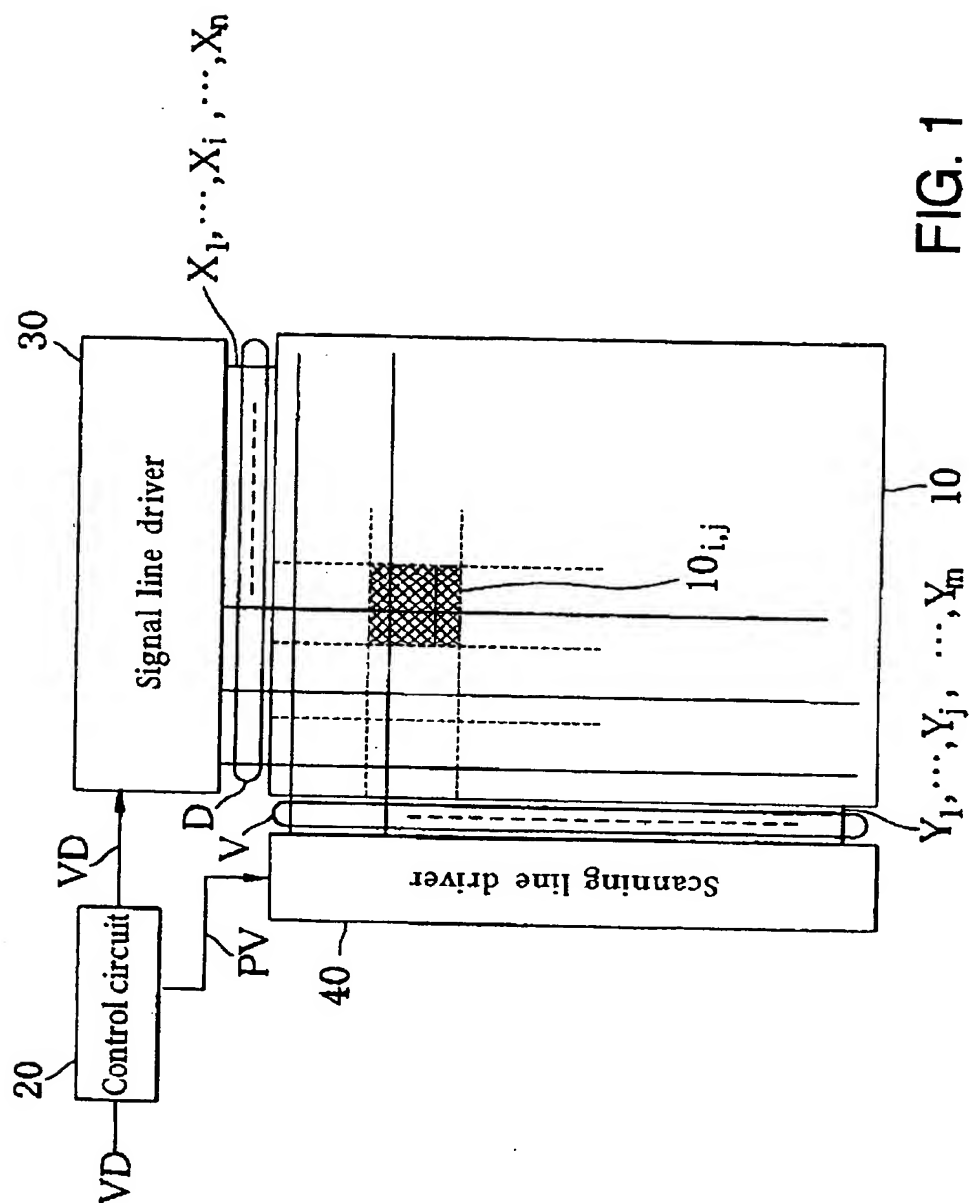


10/506371



BEST AVAILABLE COPY

10/506371

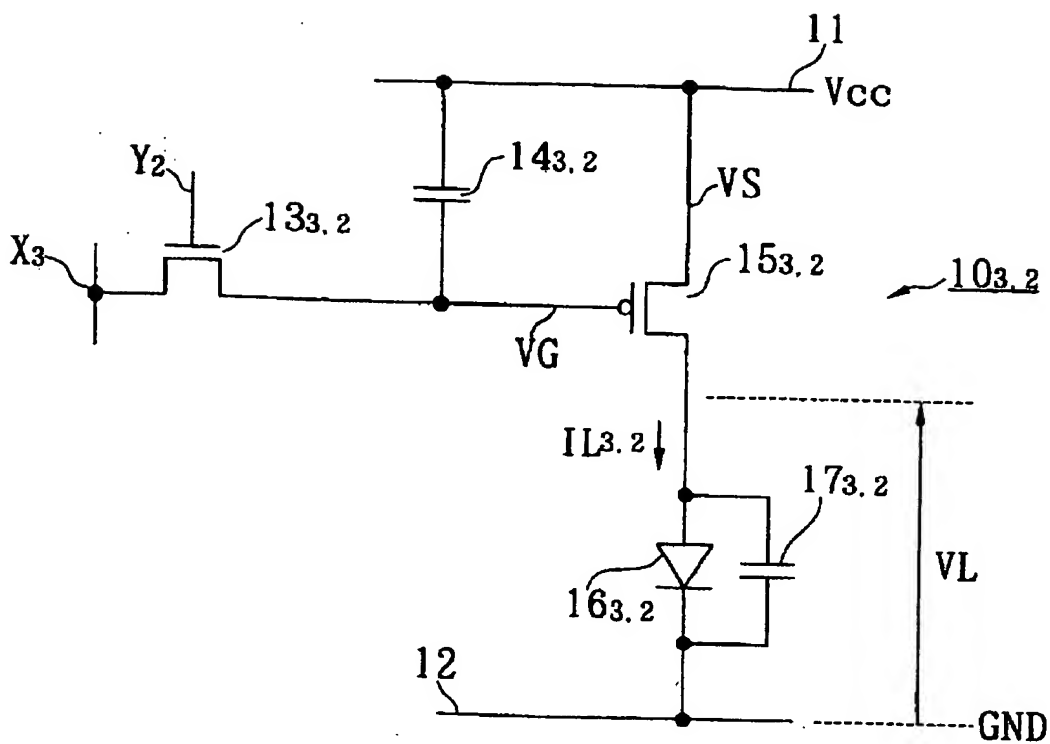


FIG. 2

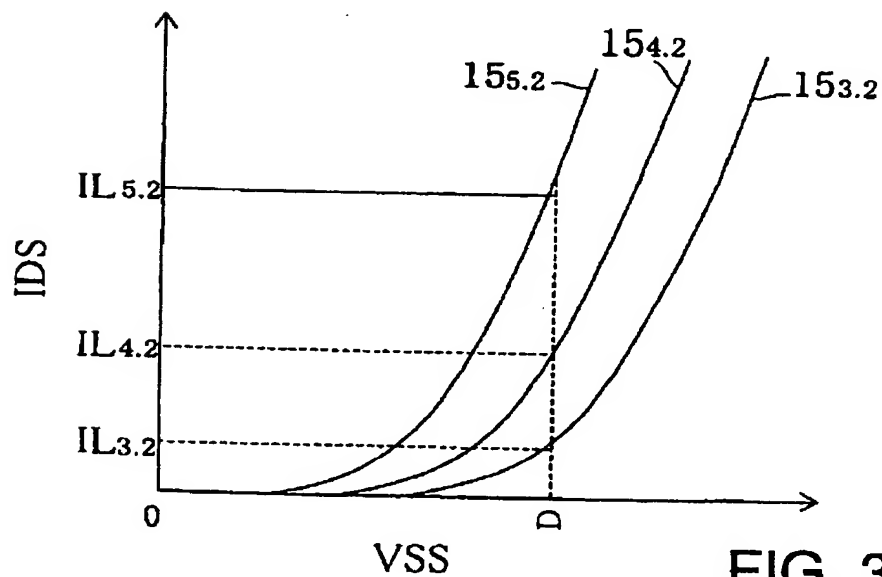
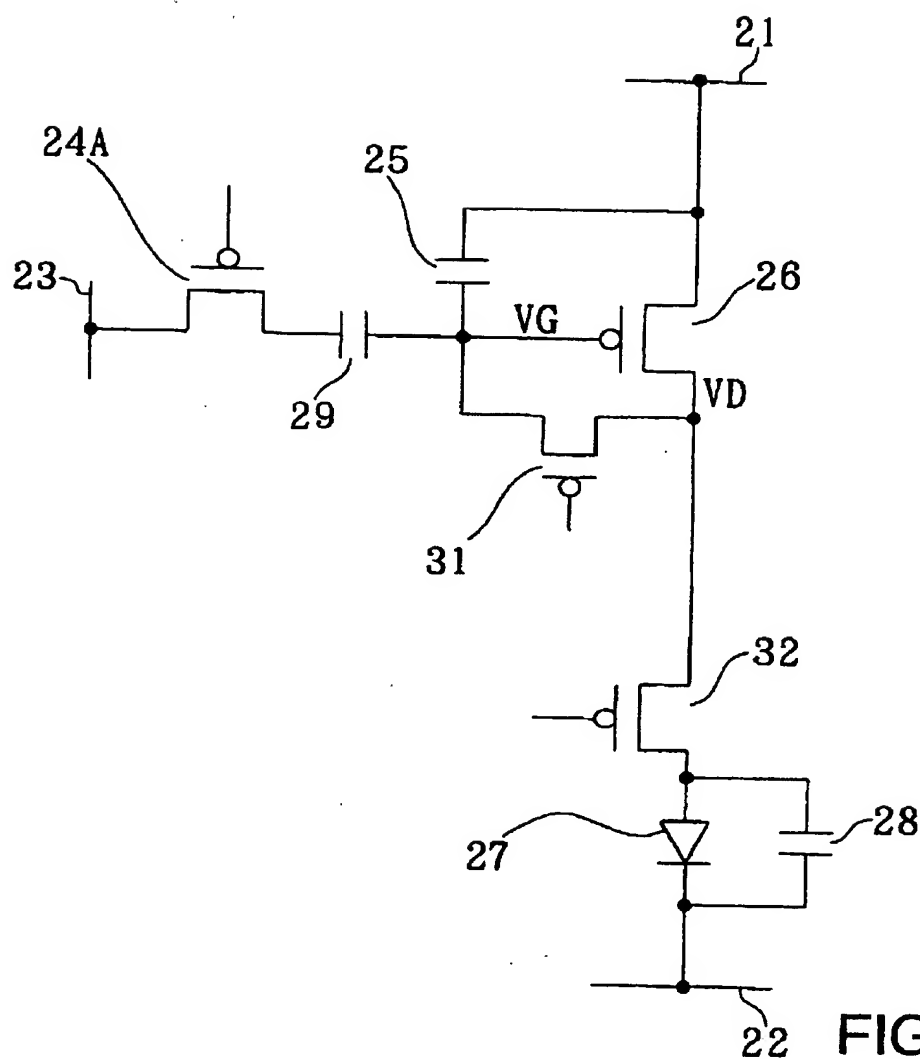


FIG. 3

10/506371



10/506371

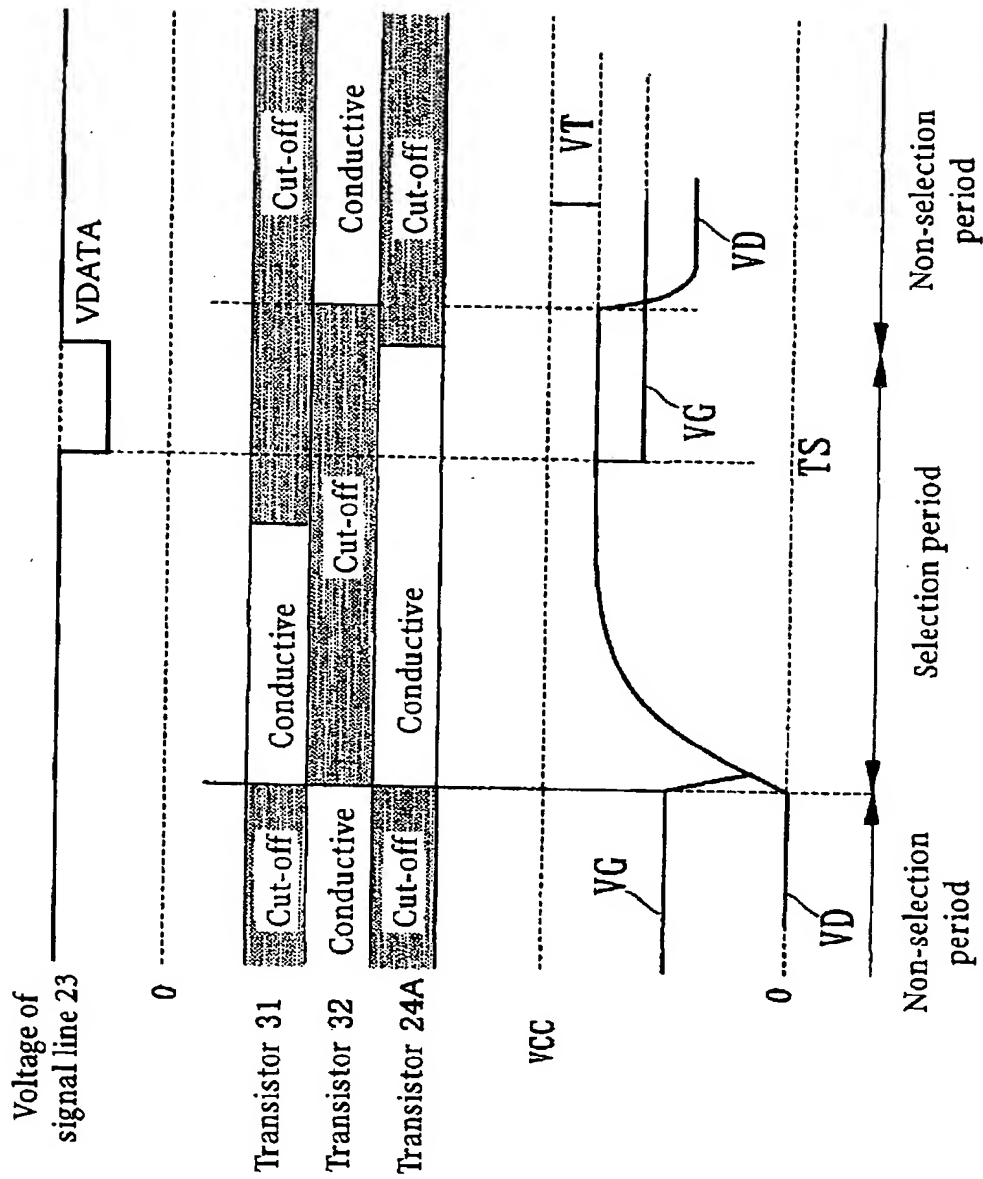
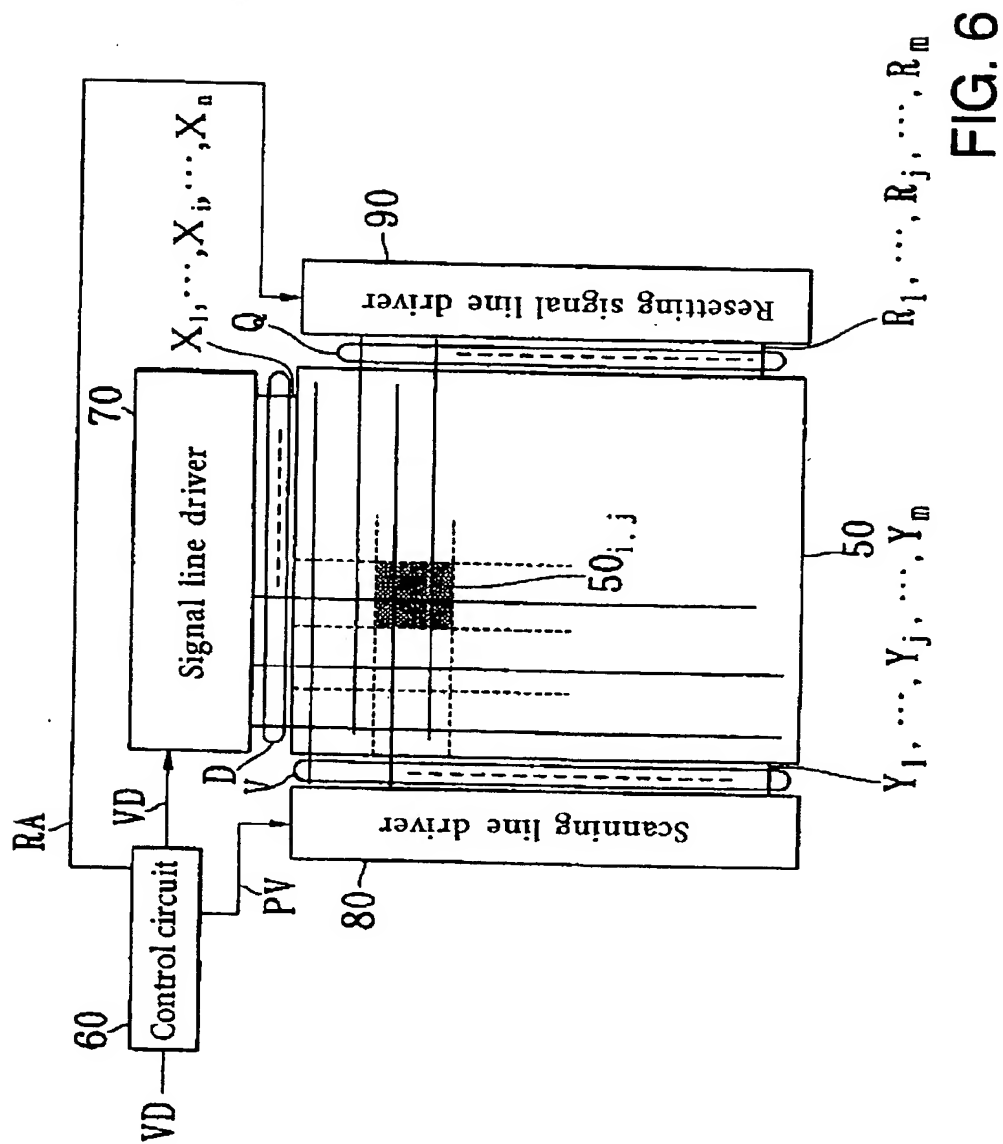
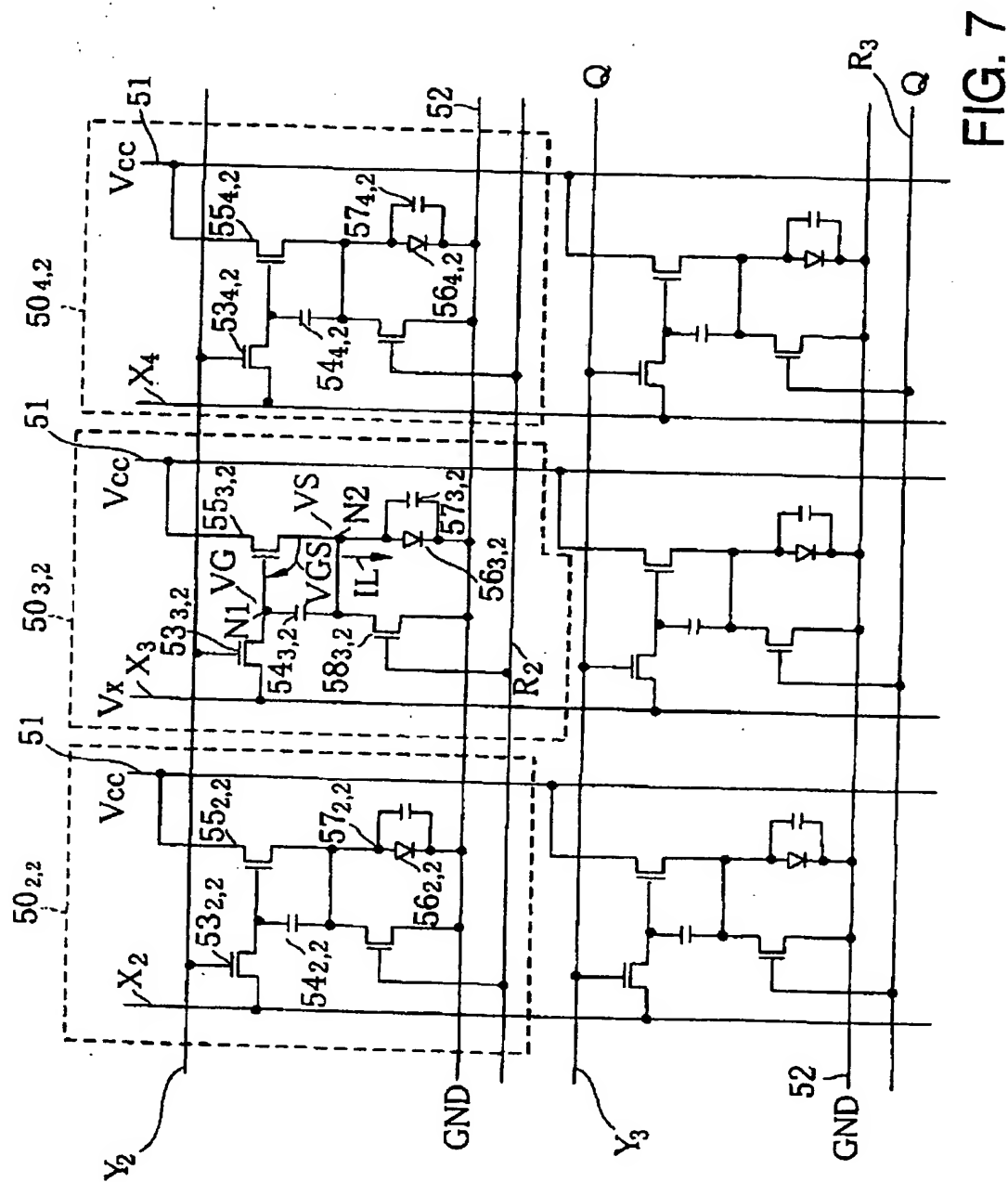


FIG. 5

10/506371



10/506371



10/506371

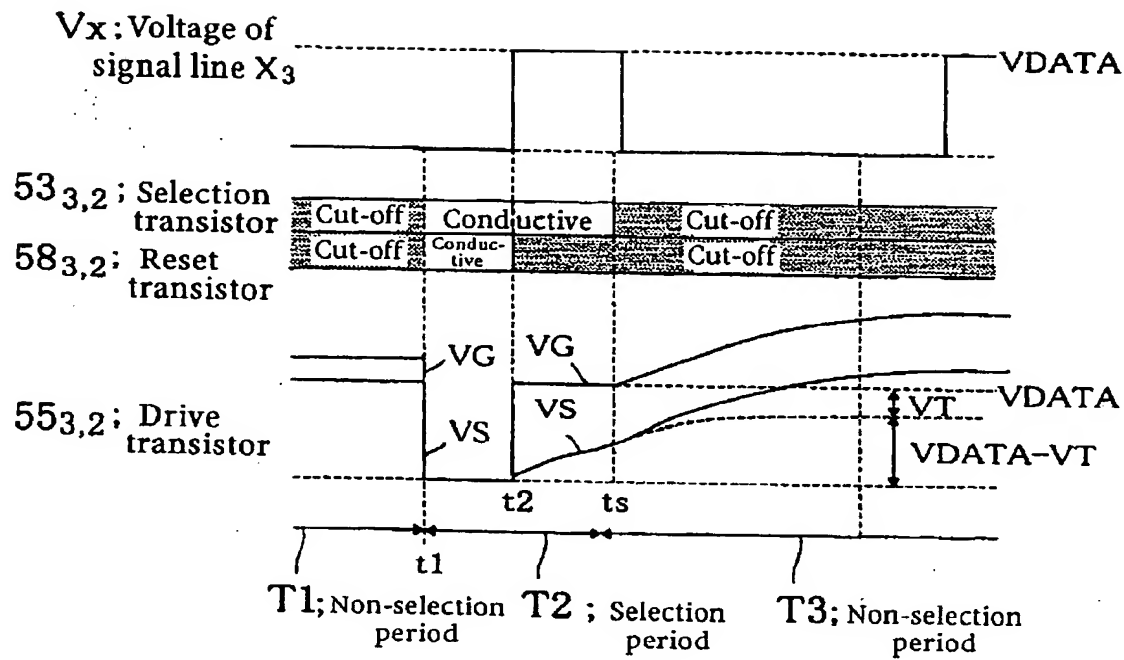


FIG. 8

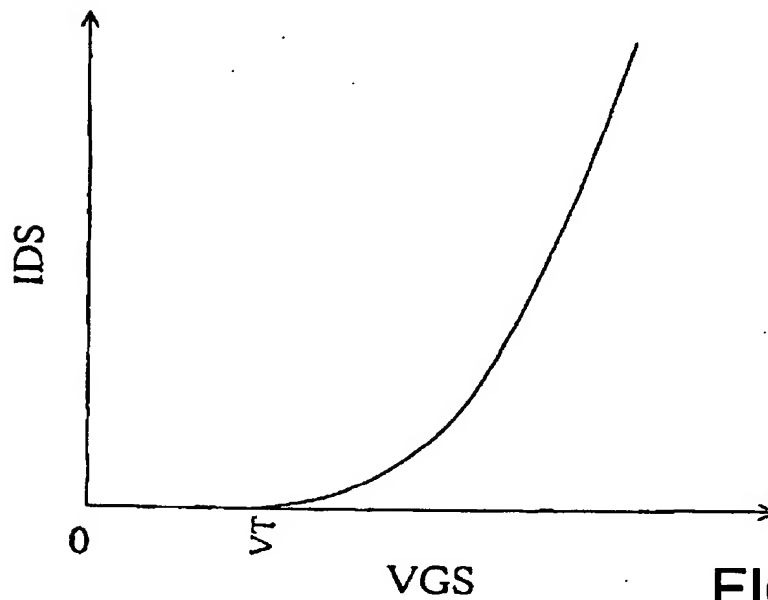


FIG. 9

10/506371

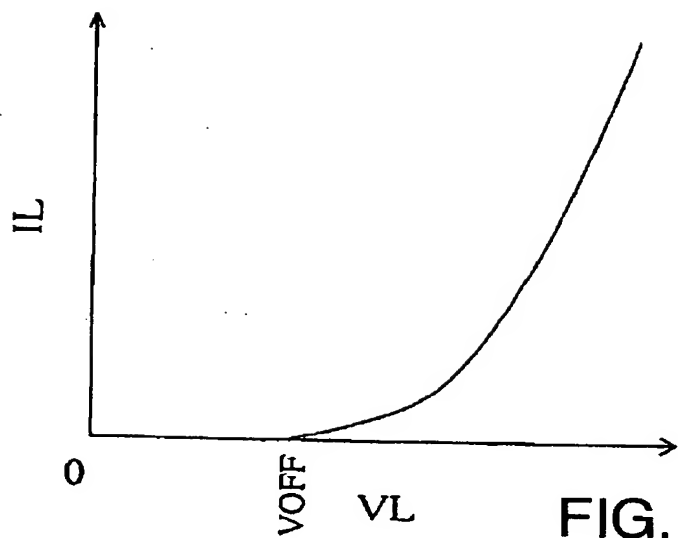


FIG. 10

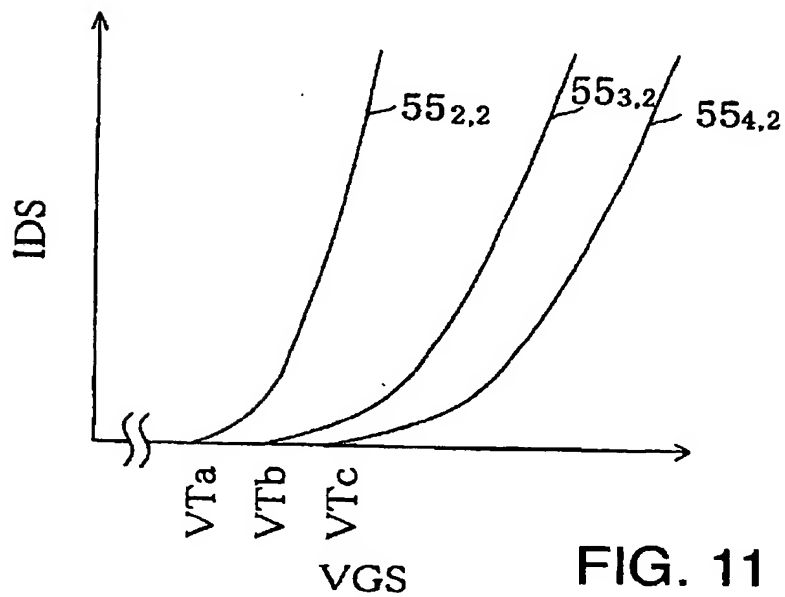


FIG. 11



10/506371

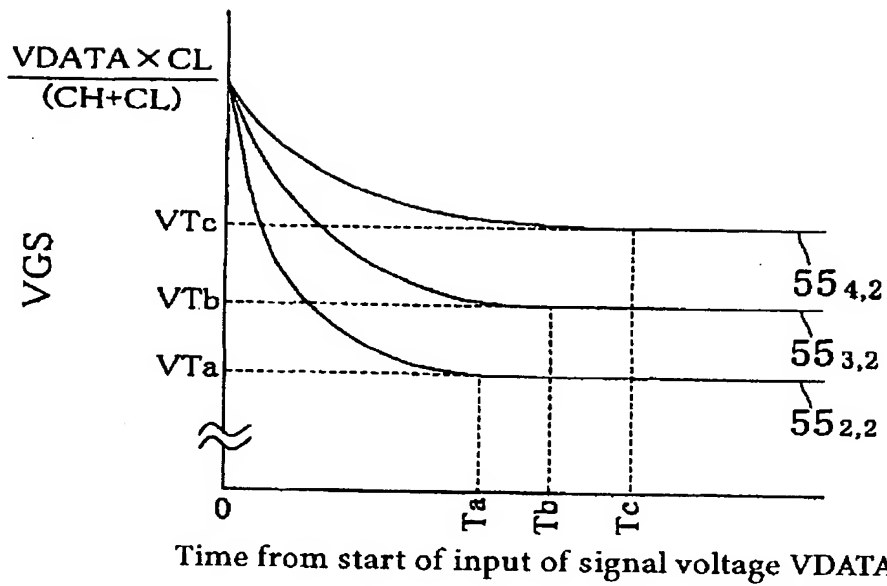


FIG. 12

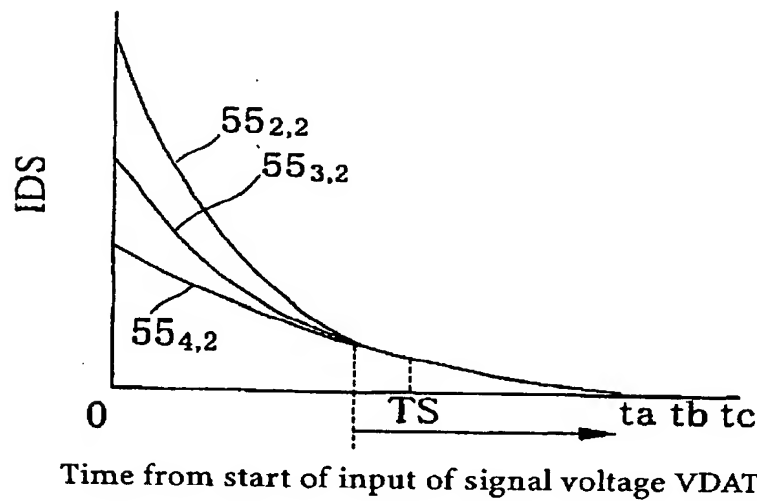


FIG. 13

10/506371

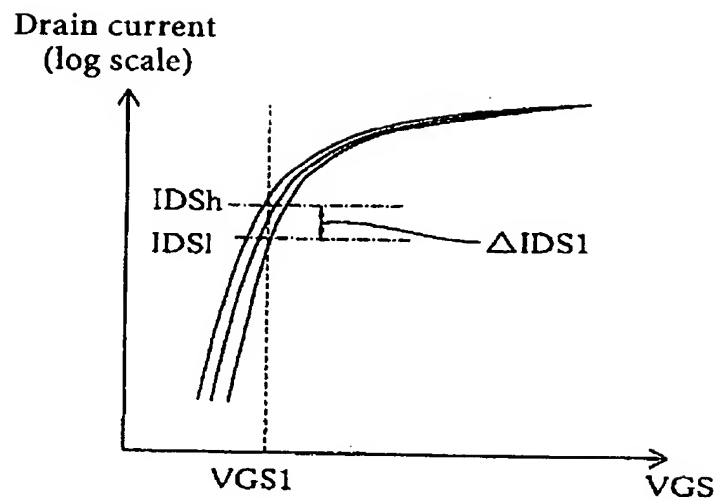


FIG. 14

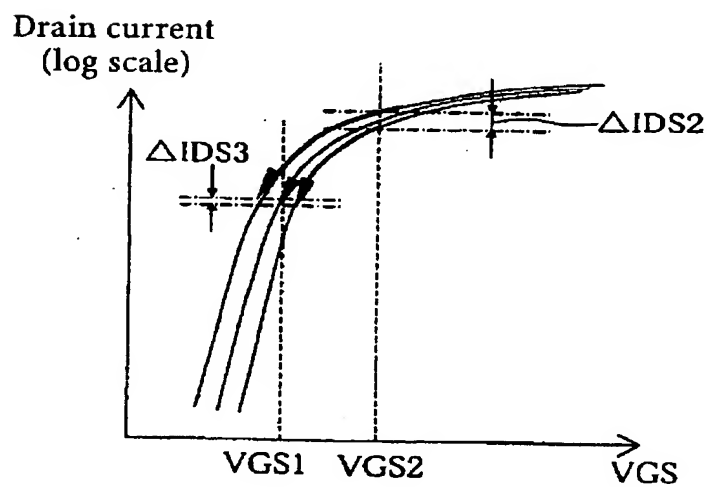


FIG. 15

10/506371

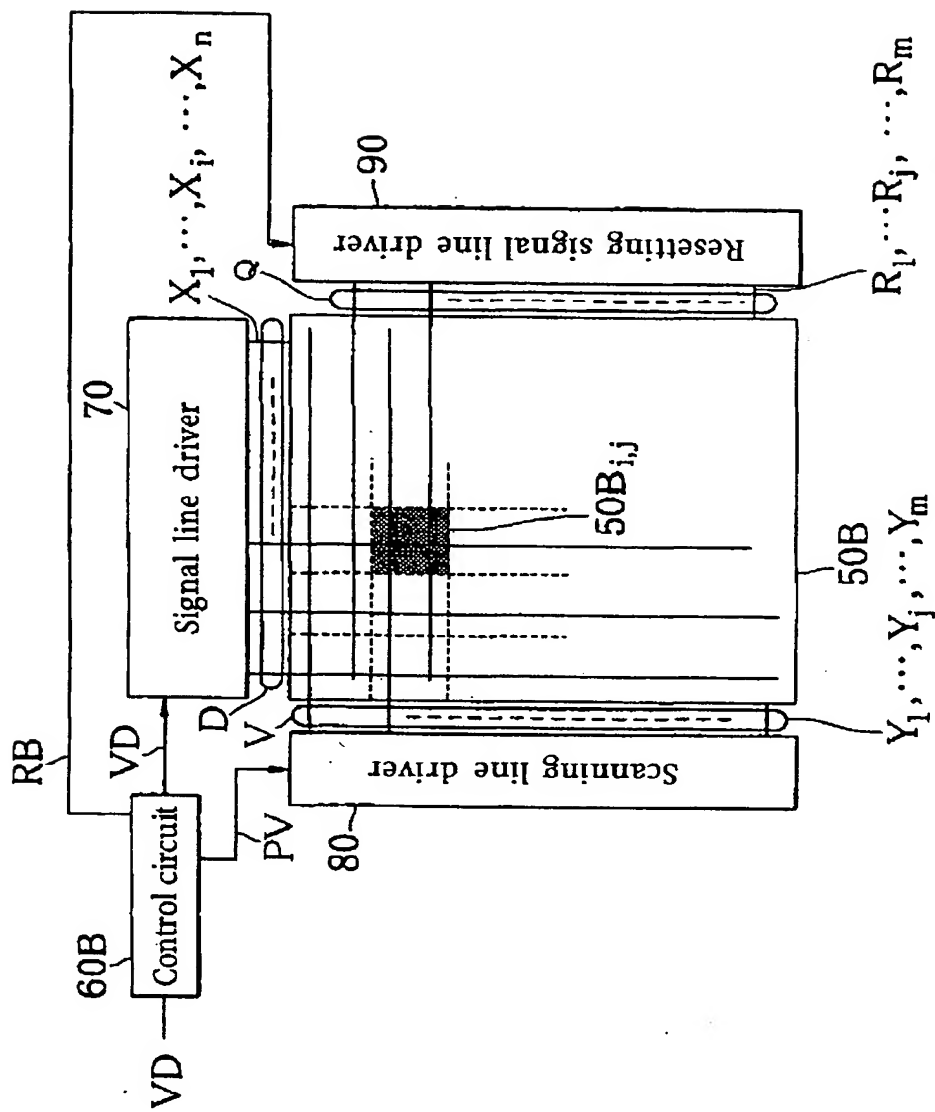


FIG. 16

10/506371

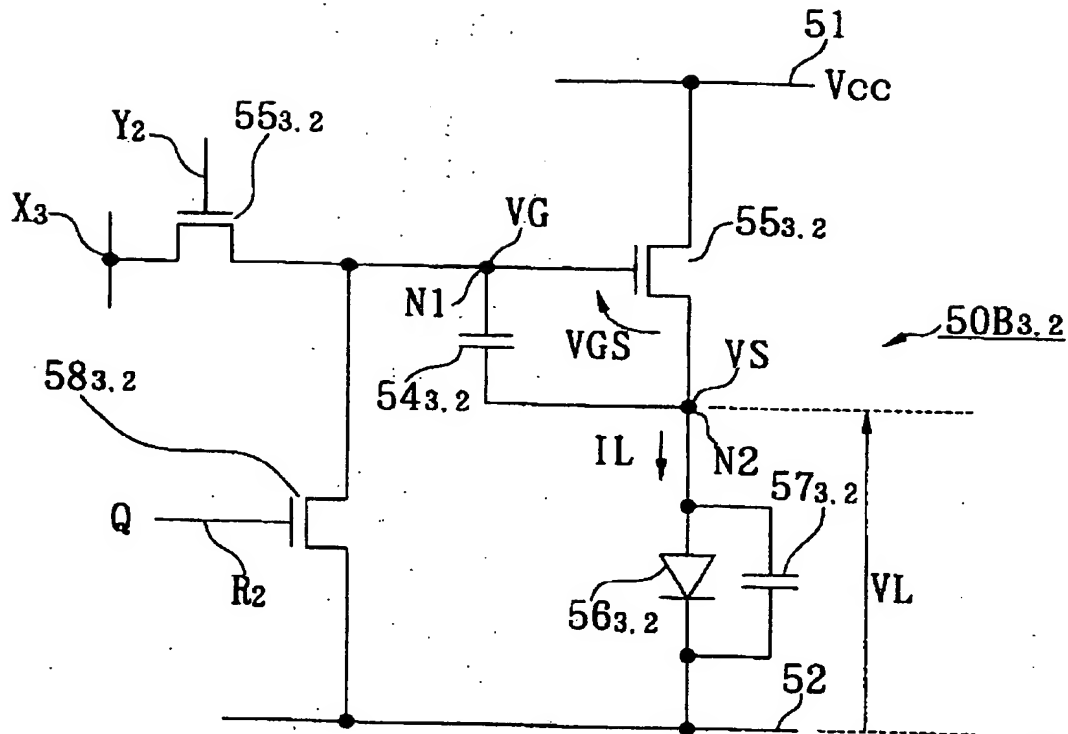


FIG. 17

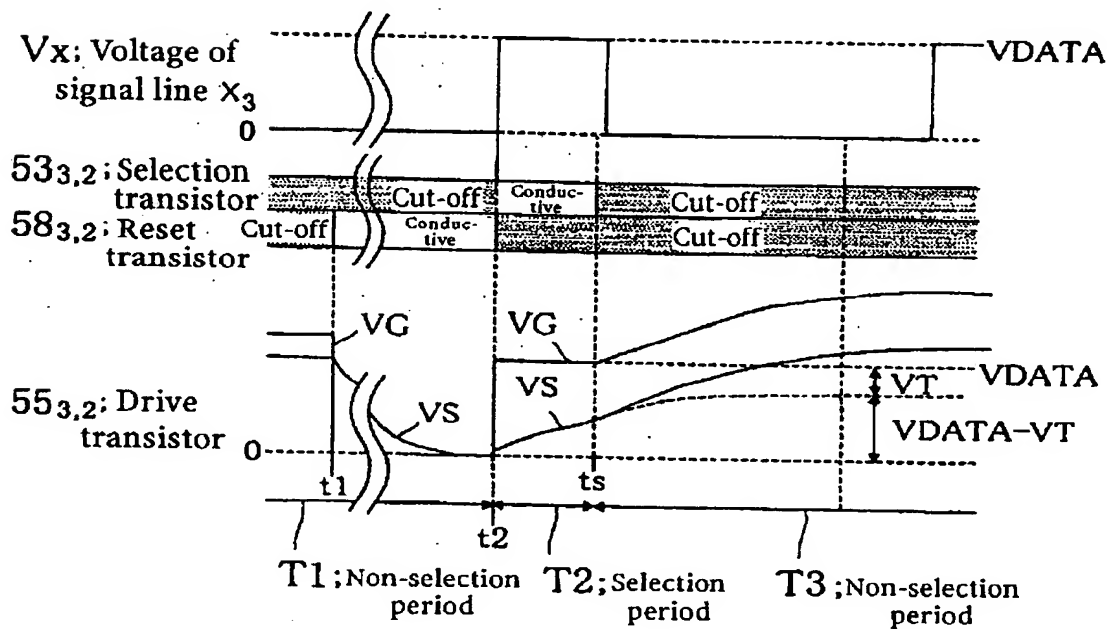


FIG. 18

10/506371

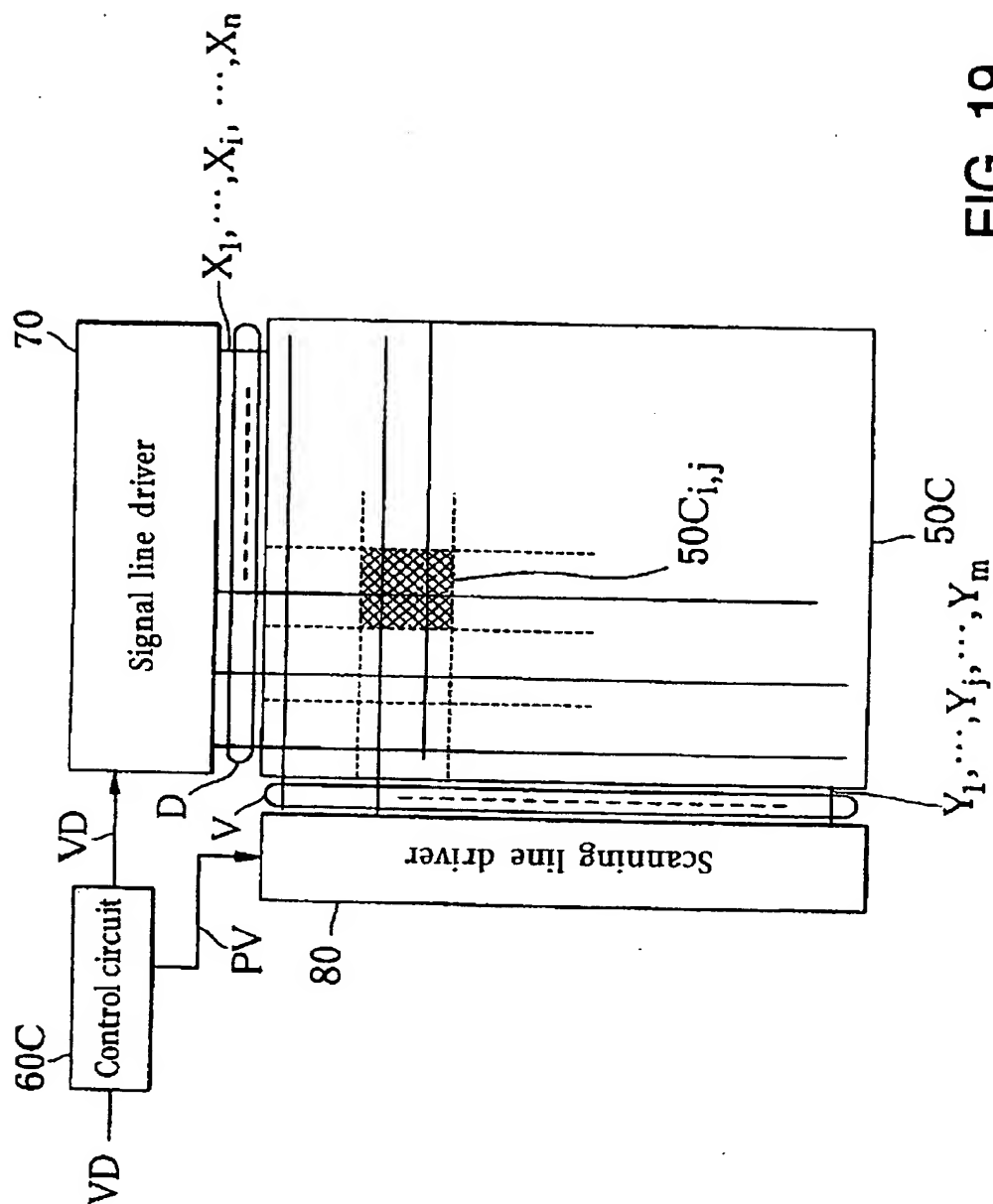


FIG. 19

**10/506371**

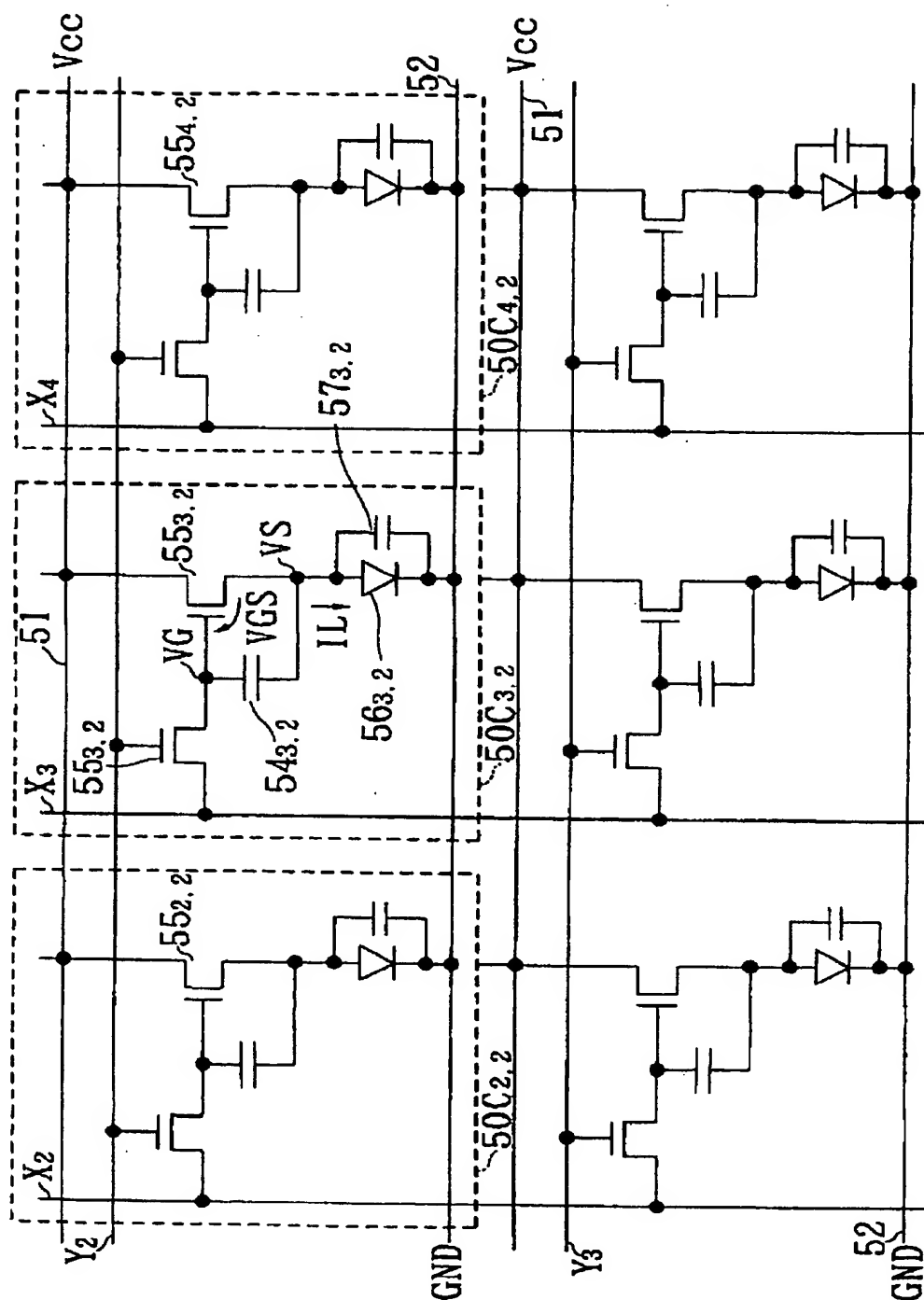


FIG. 20

10/506371

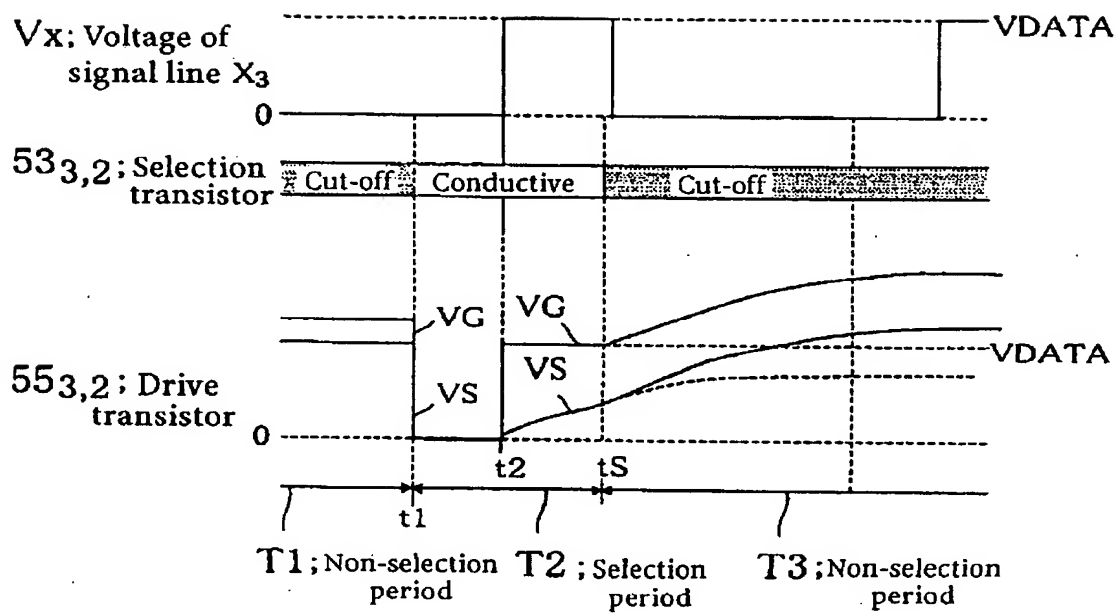


FIG. 21

10/506371

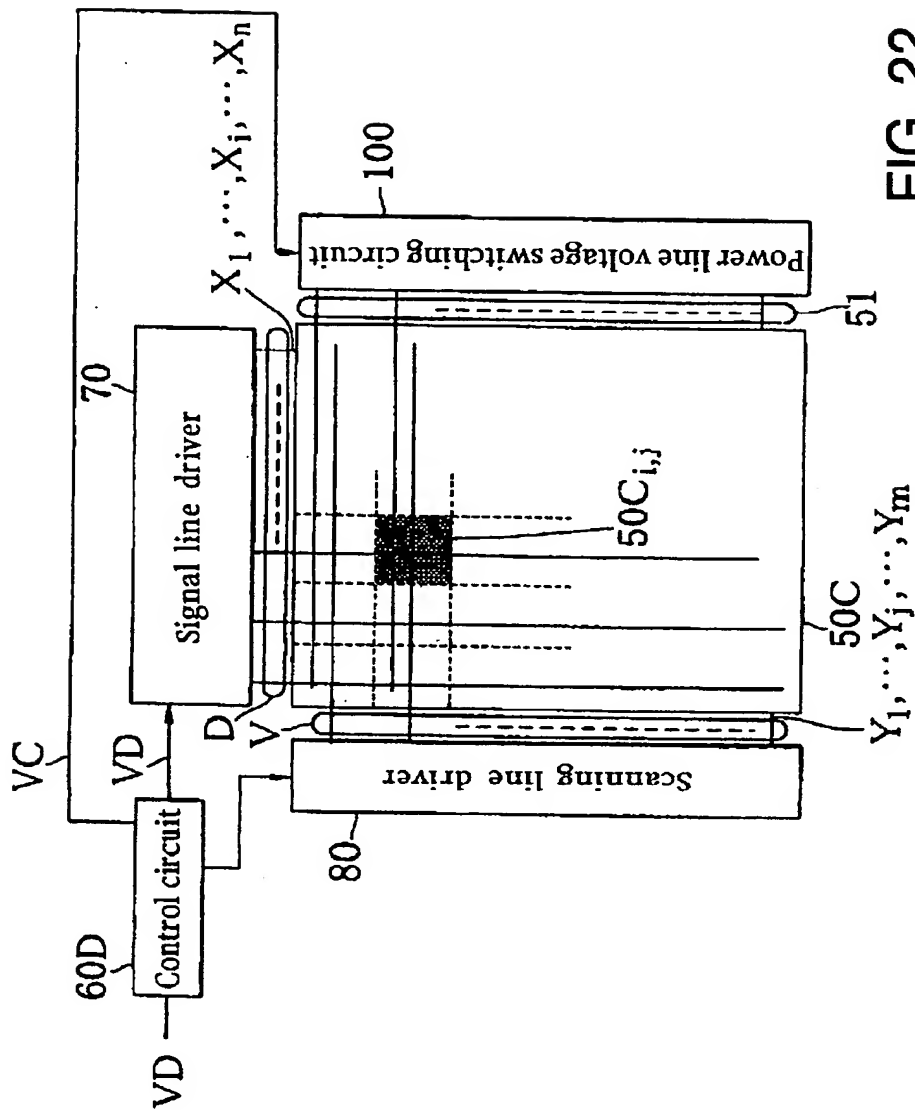
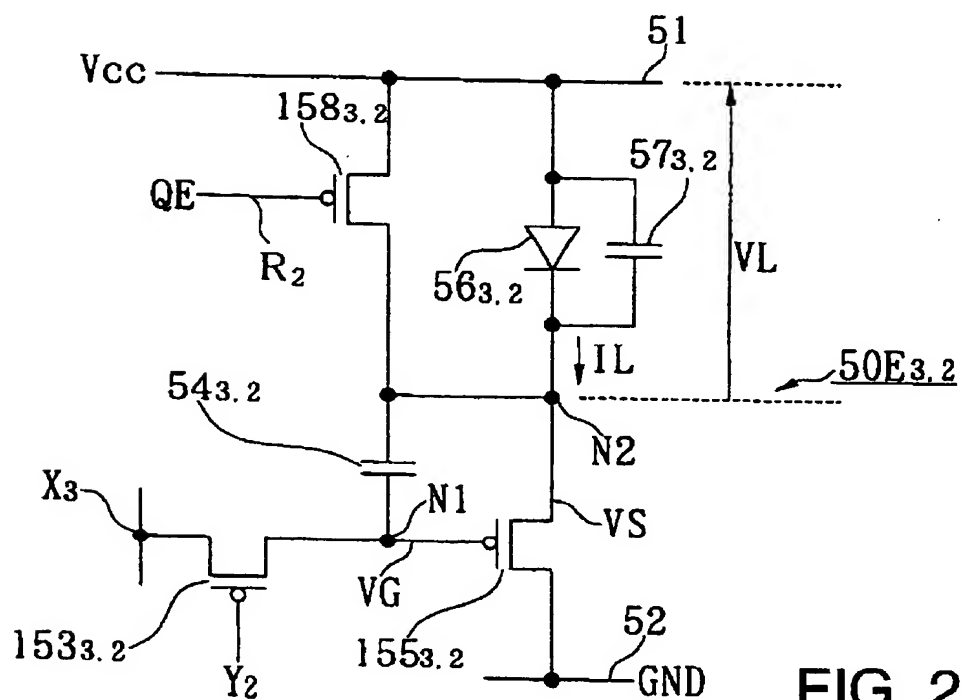
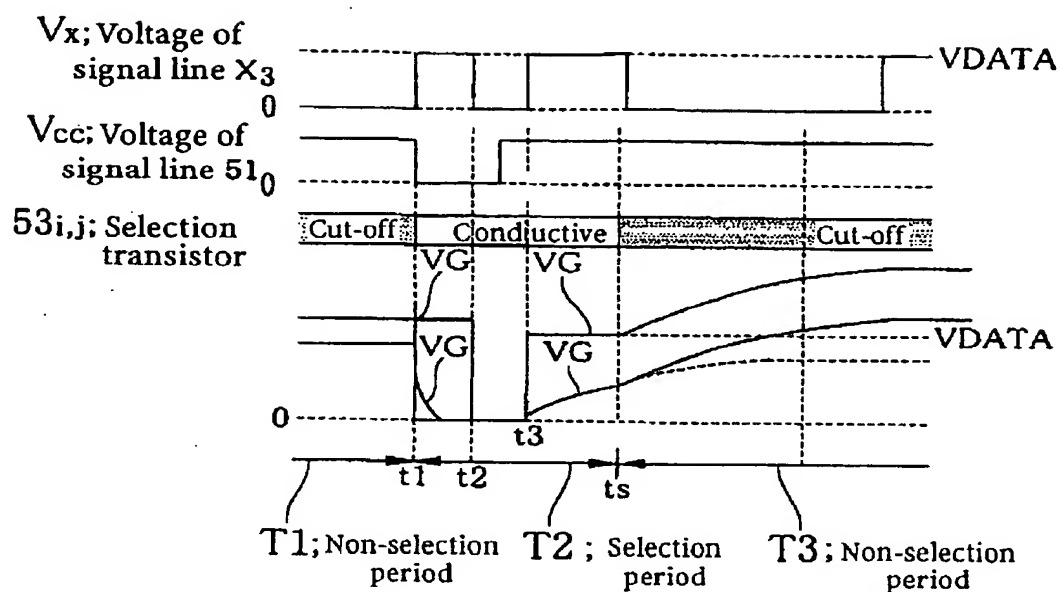


FIG. 22



10/506371



10/506371

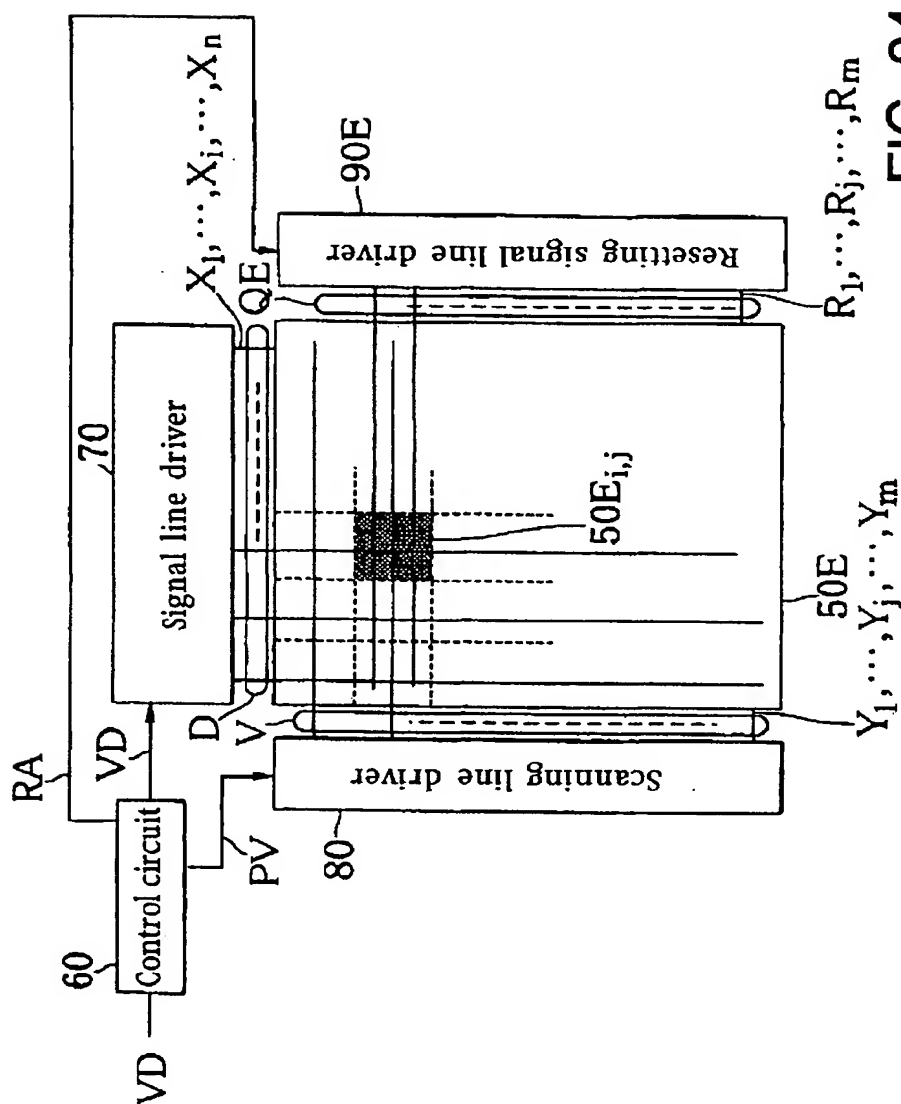


FIG. 24

10/506371

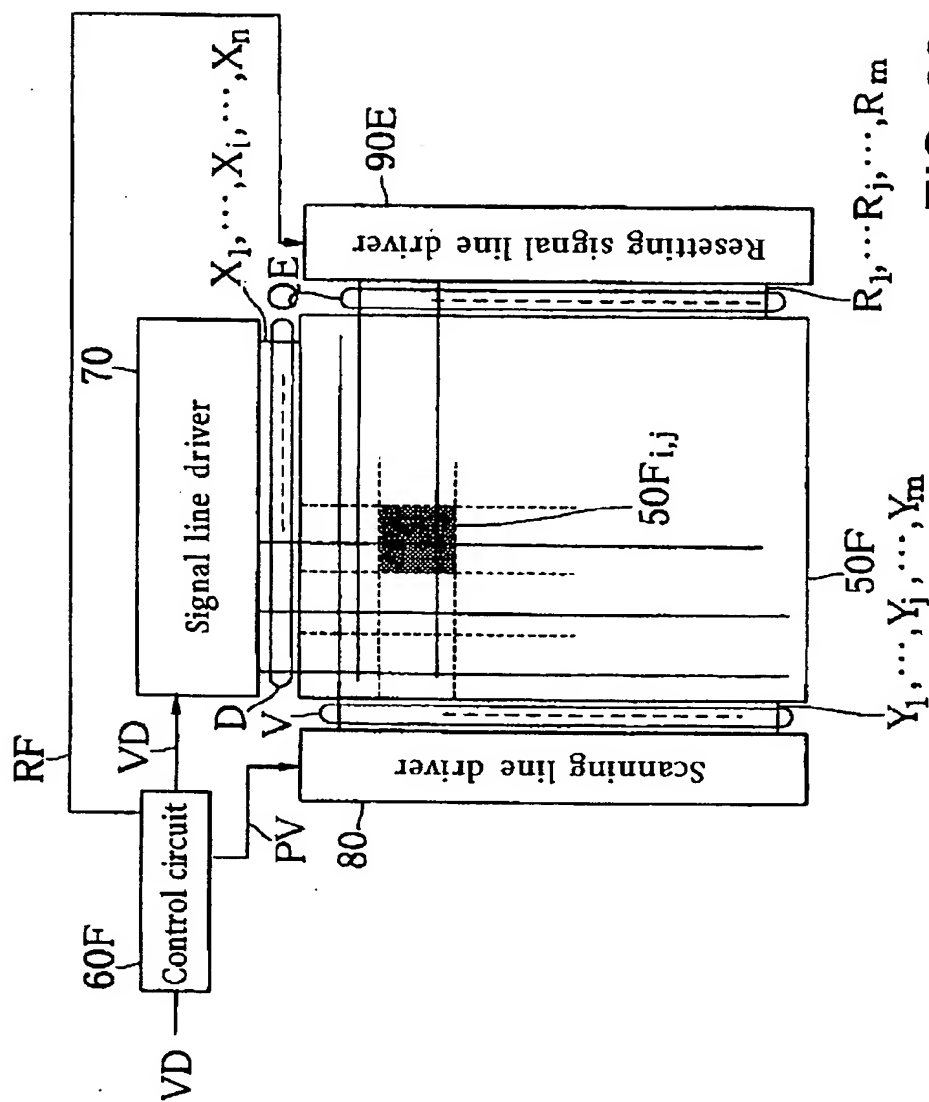
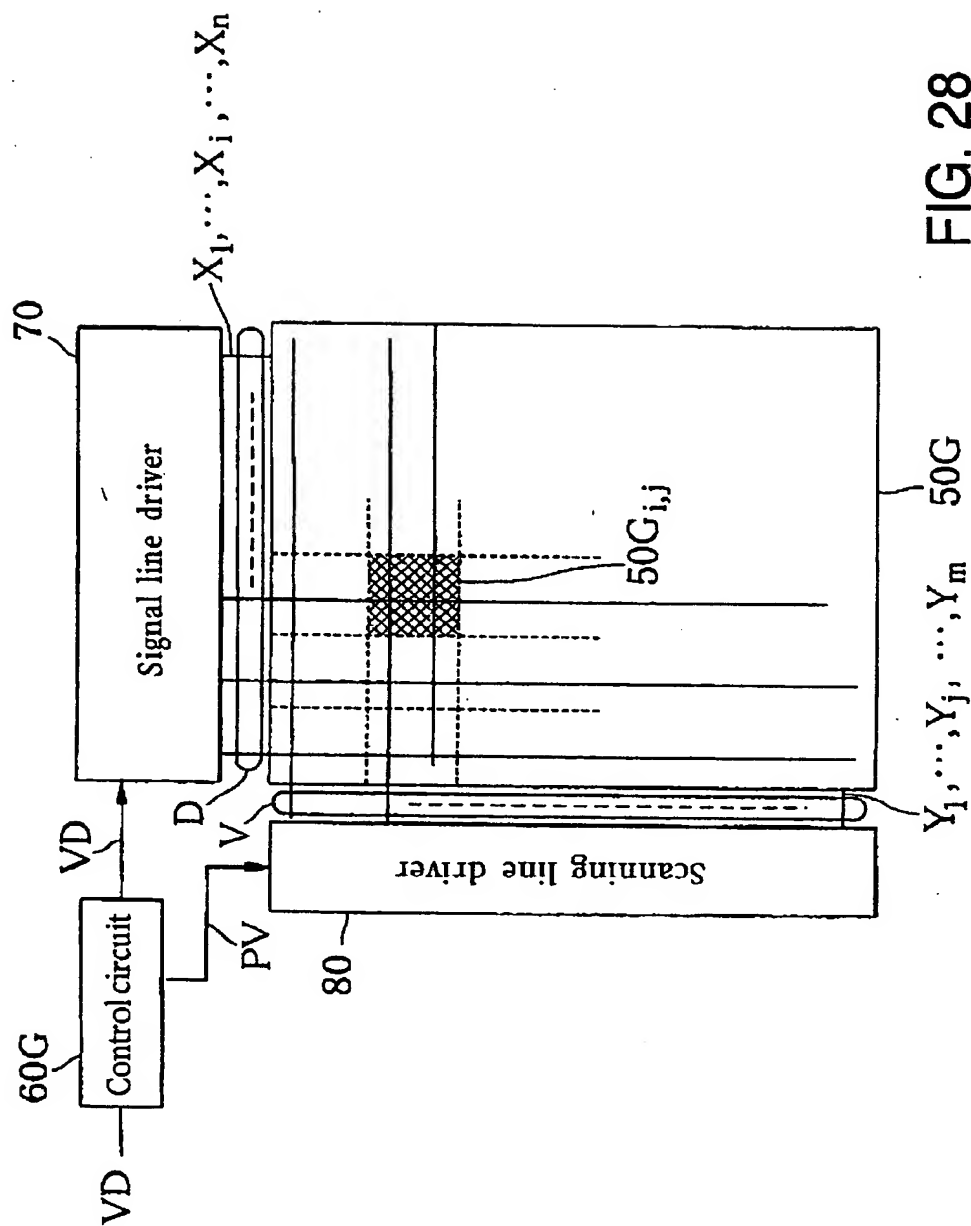


FIG. 26

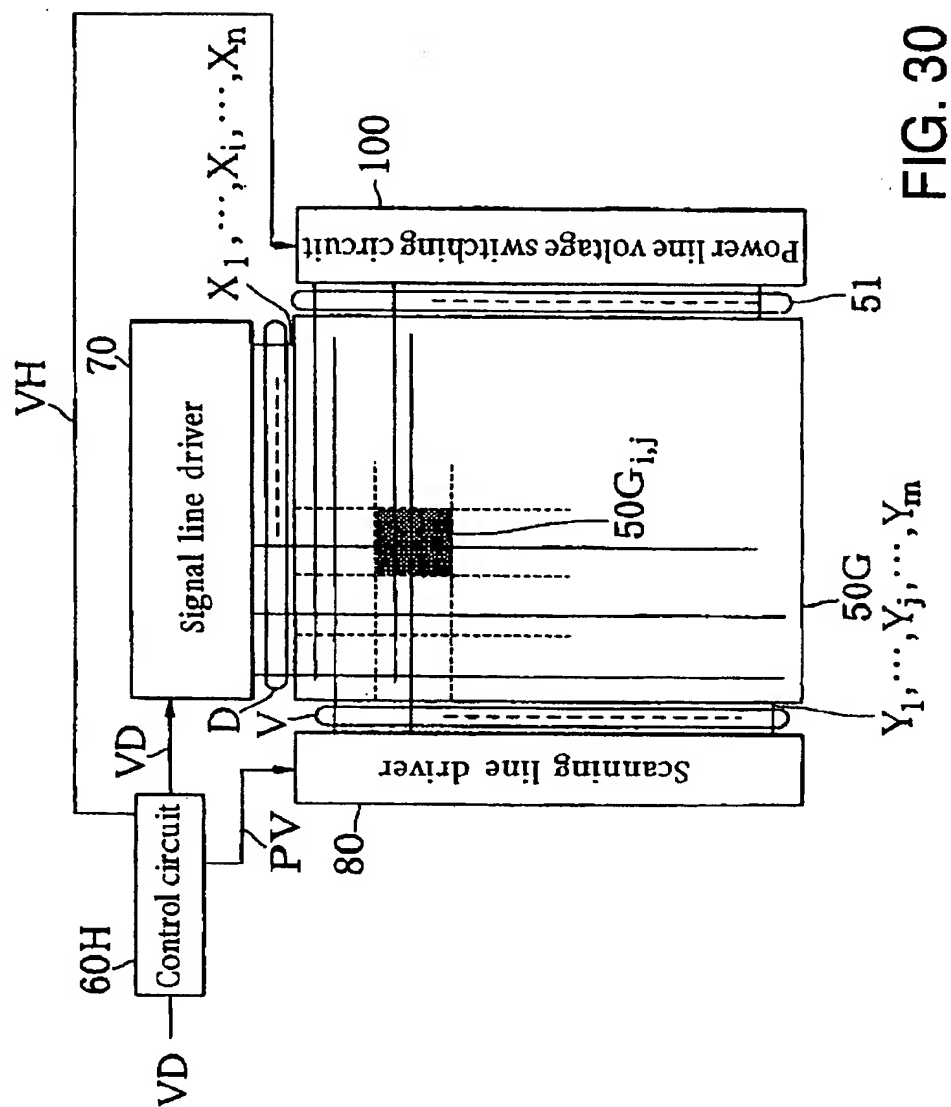
FIG. 27



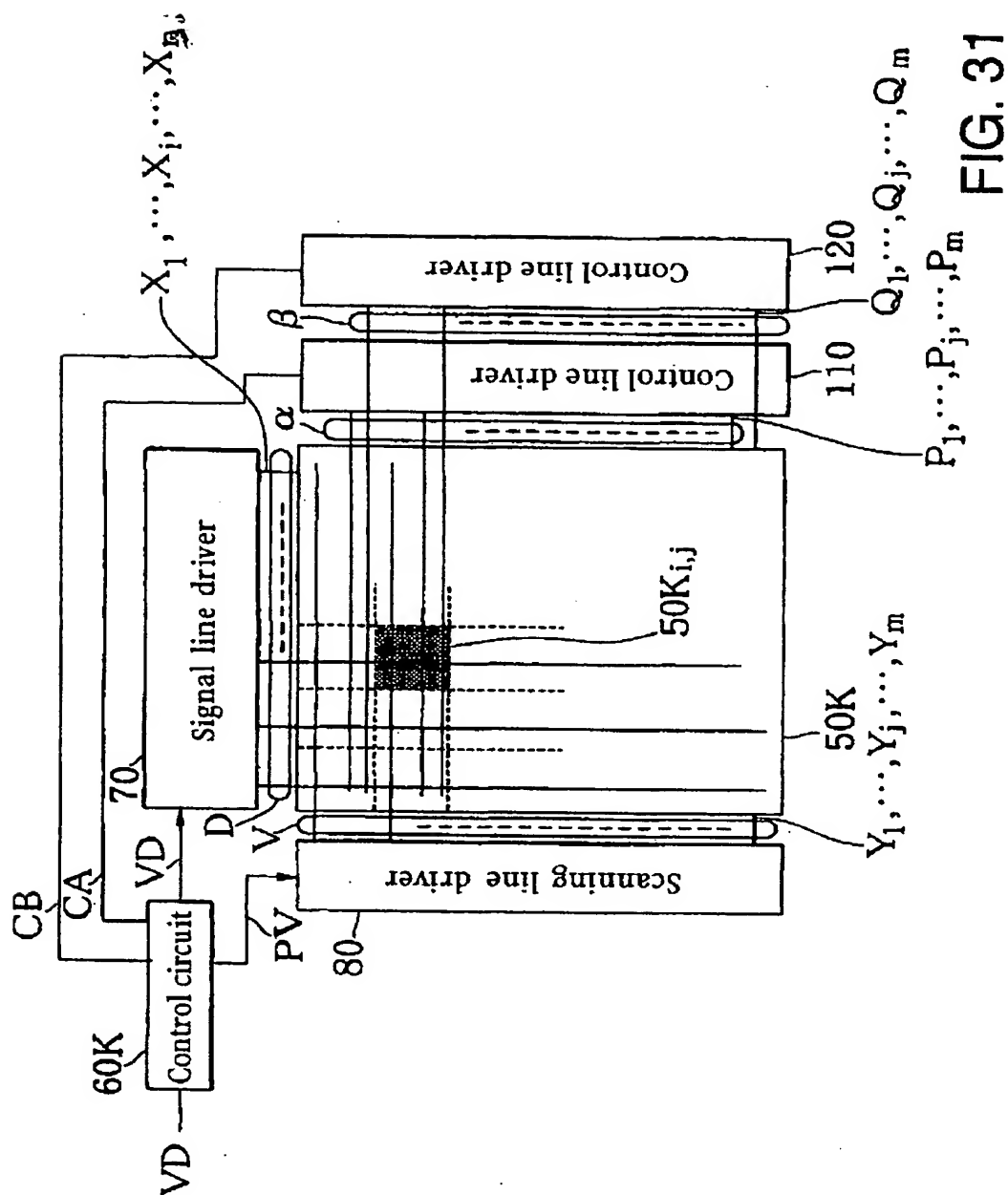
10/506371



10/506371



10/506371



[illegible]

Vx; x 3  
0

153<sub>3,2</sub>: Selection transistor  
158<sub>3,2</sub>: Control transistor  
159<sub>3,2</sub>: pMOS

Cut-off Conductive Cut-off  
Cut-off Conductive Cut-off  
Cut-off Cut-off Conductive

VG VDATA

155<sub>3,2</sub>: Drive transistor

0

t1 ts t2

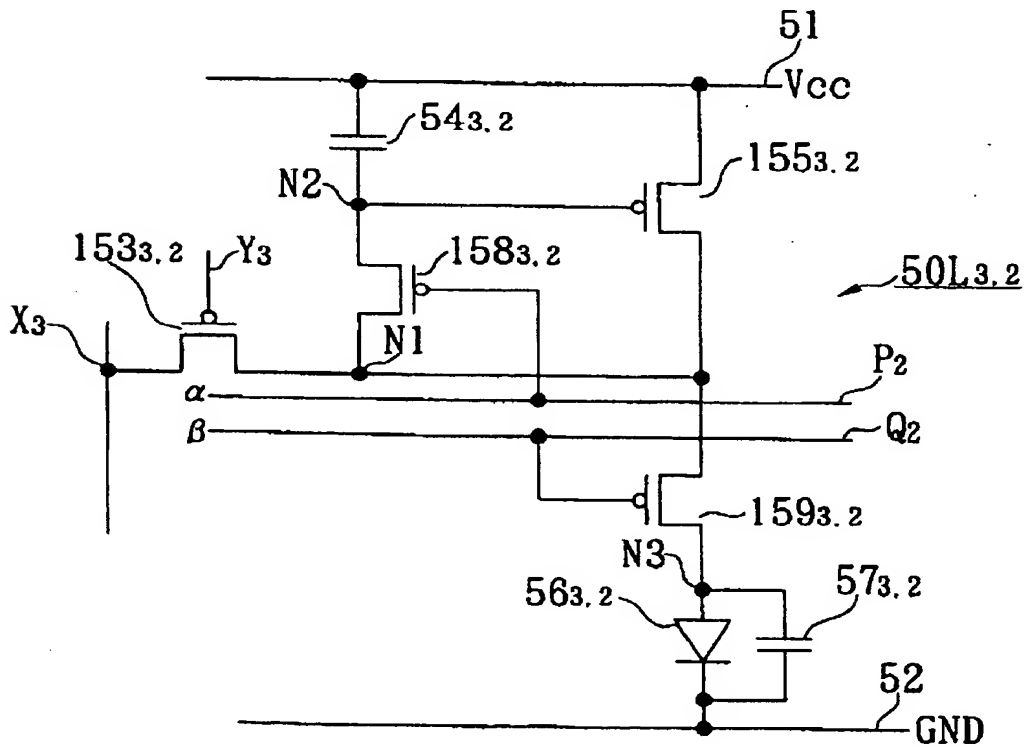
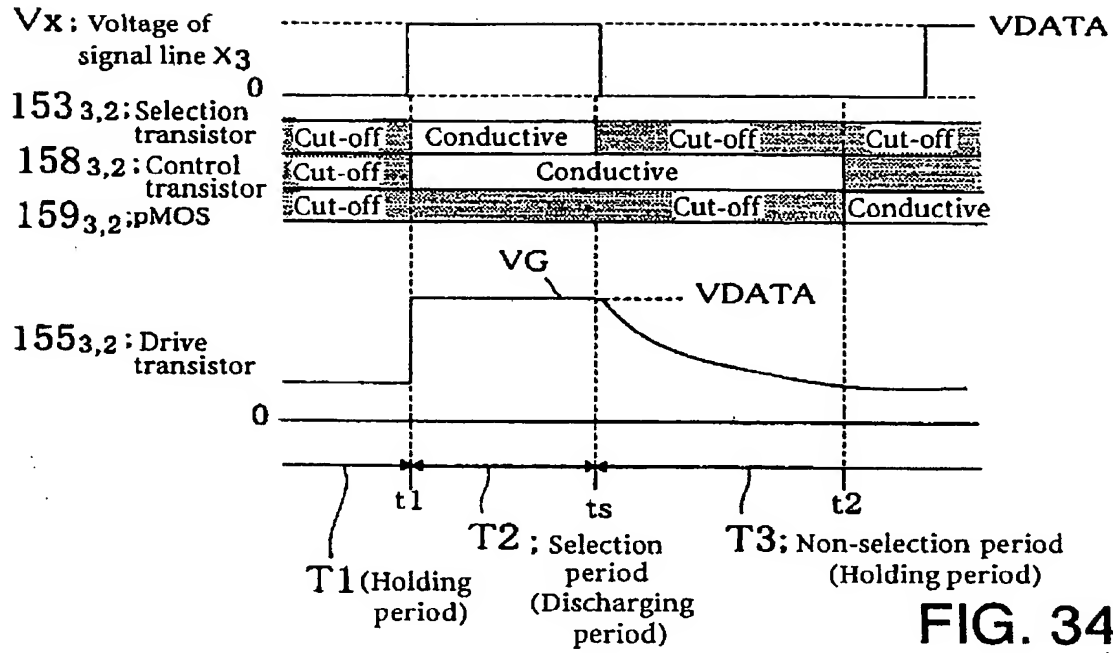
T1 (Holding period) T2 ; Selection period (Discharging period) T3; Non-selection period (Holding period)

FIG. 32

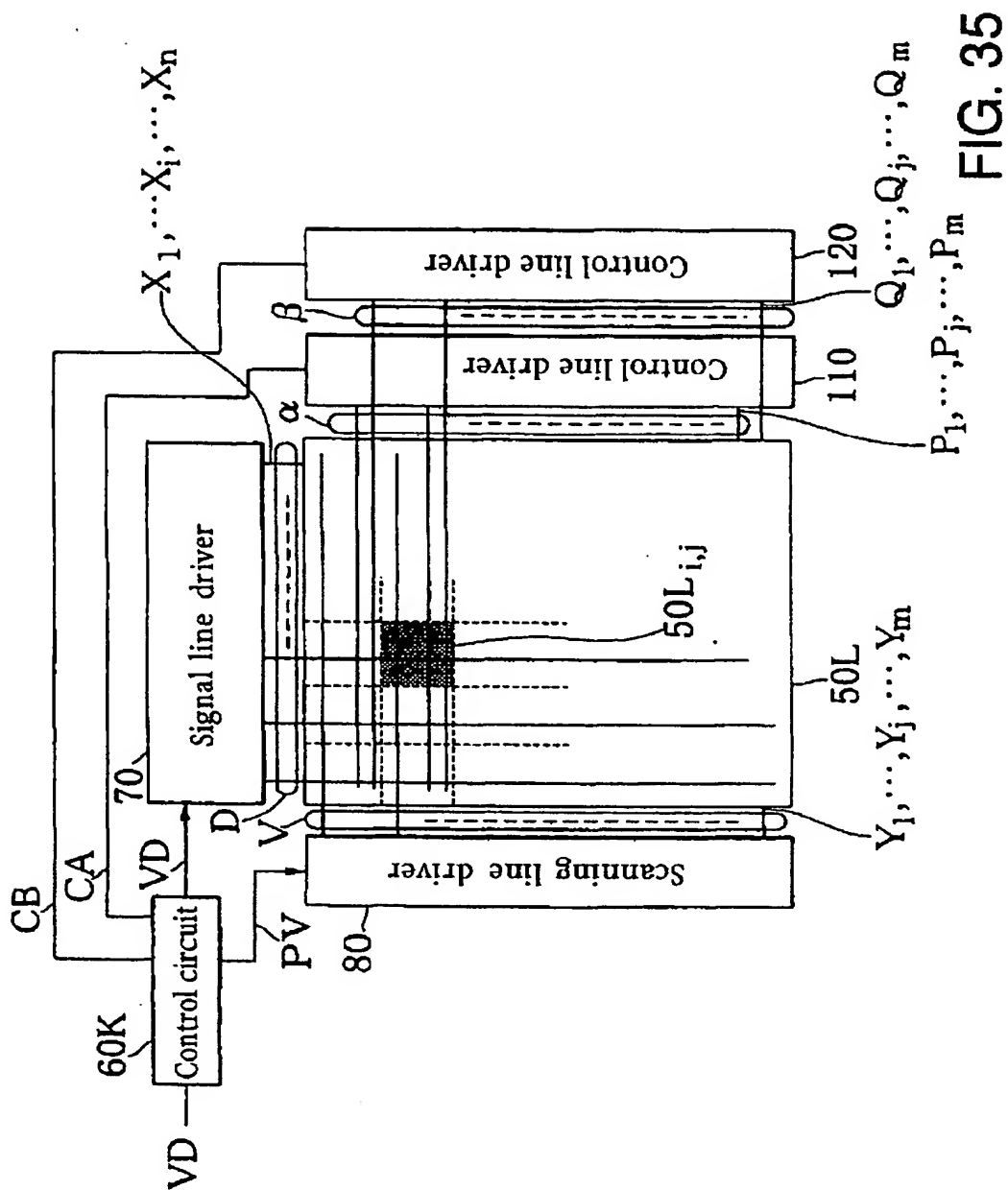
FIG. 33



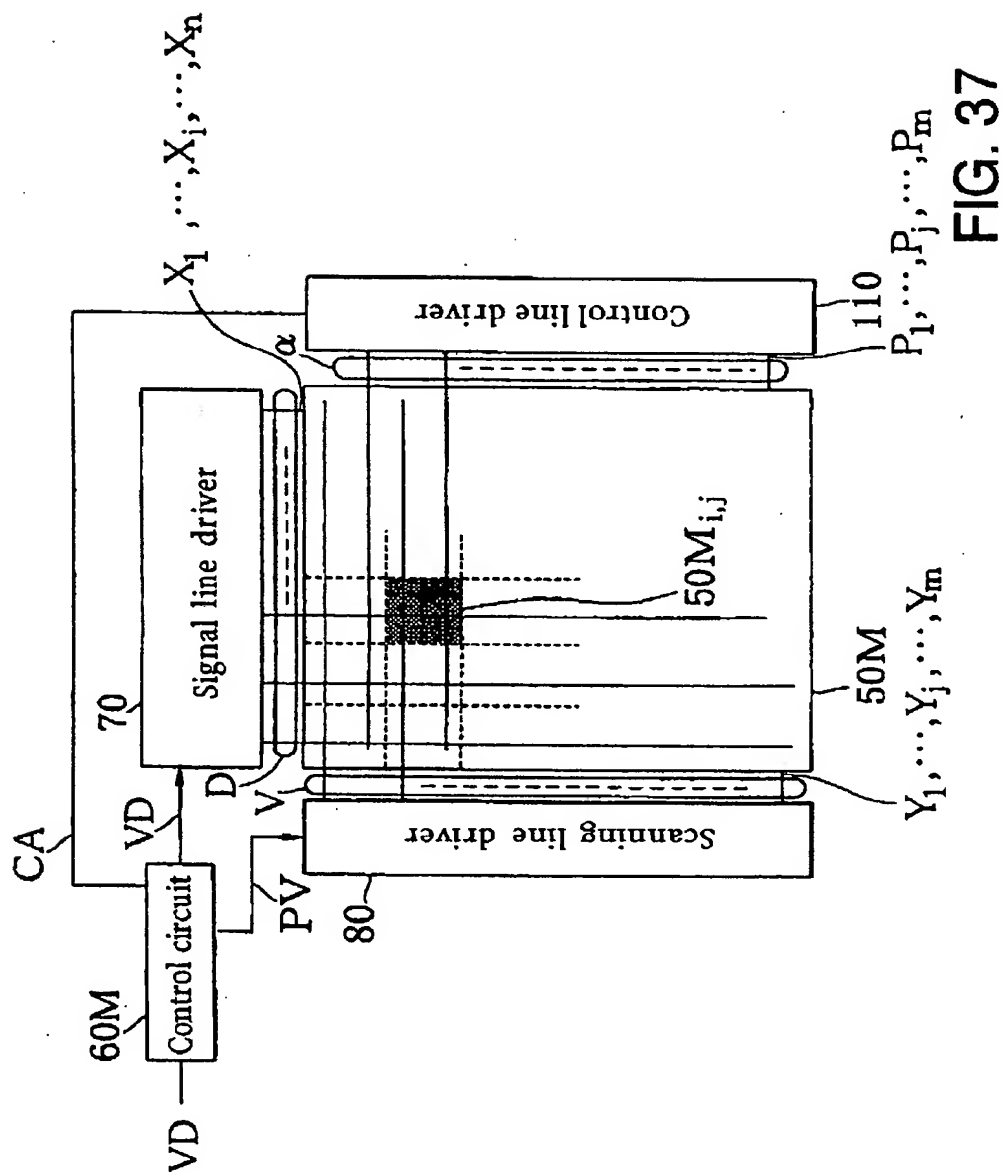
10/506371



10/506371



10/506371



10/506371

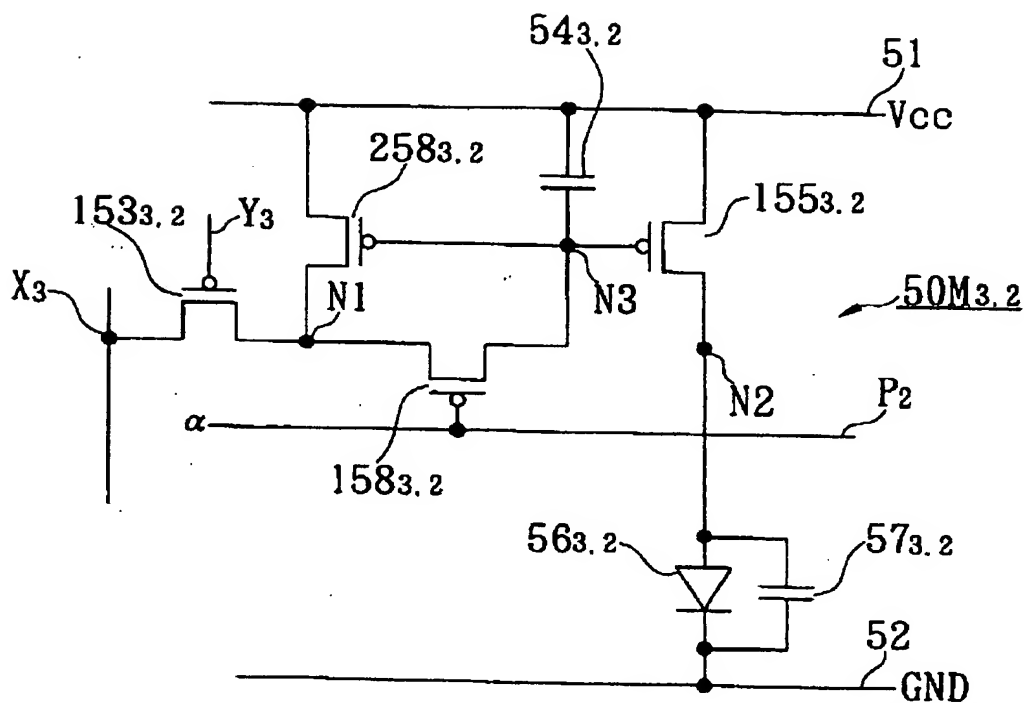


FIG. 38

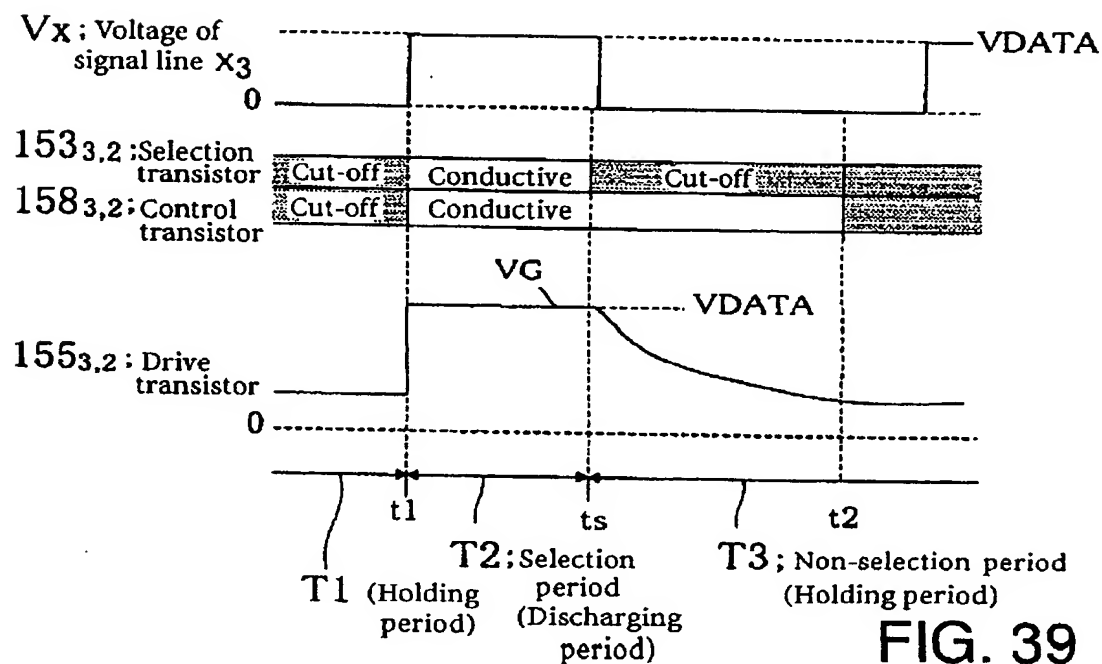
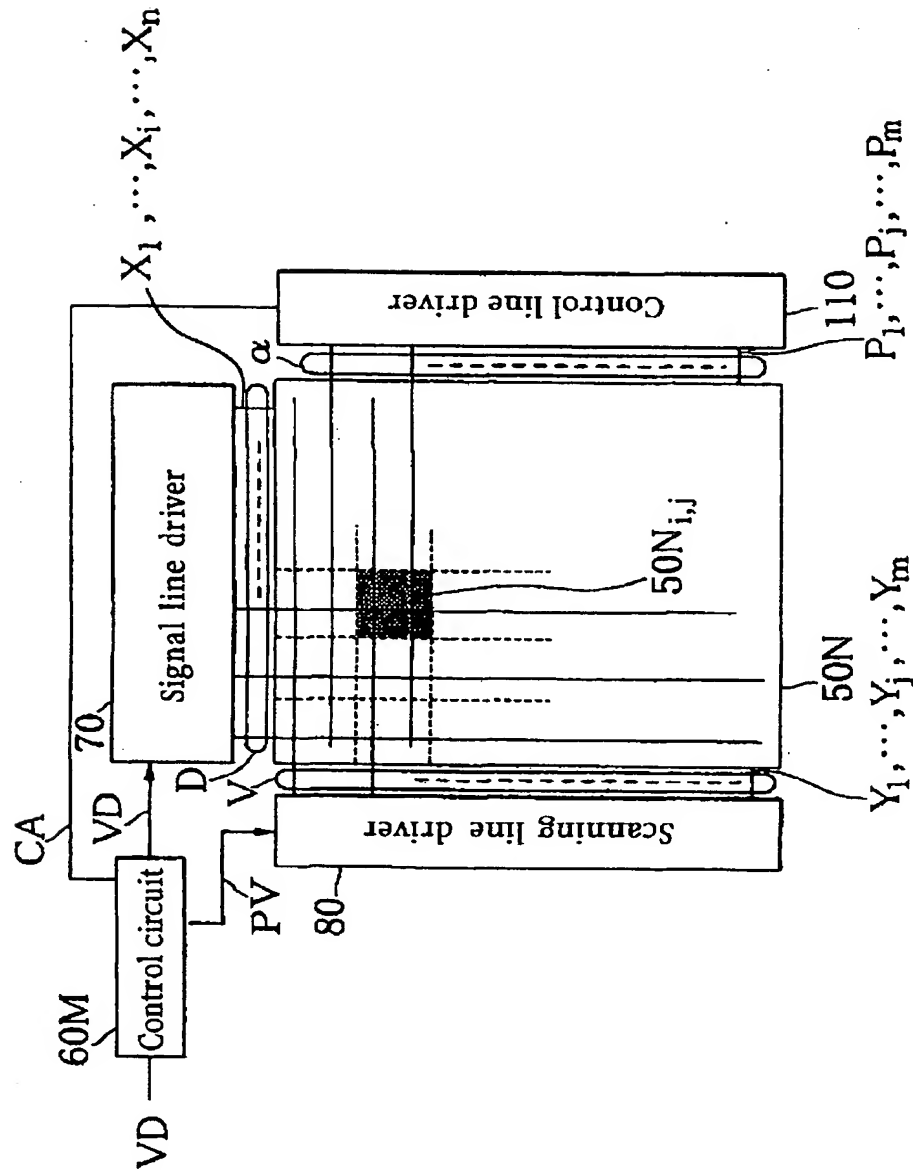


FIG. 39

10/506371



**FIG. 40**

FIG. 42

10/506371

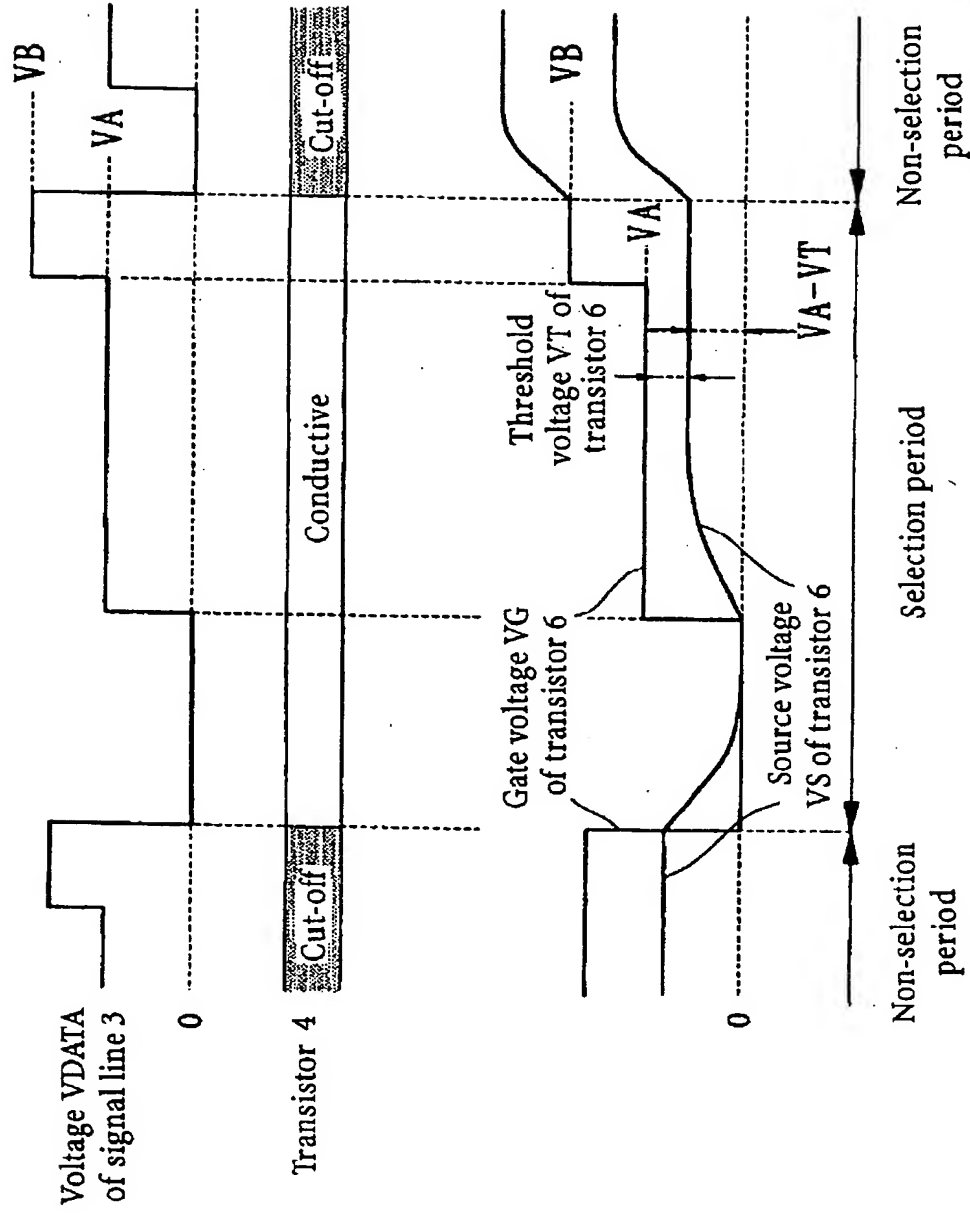
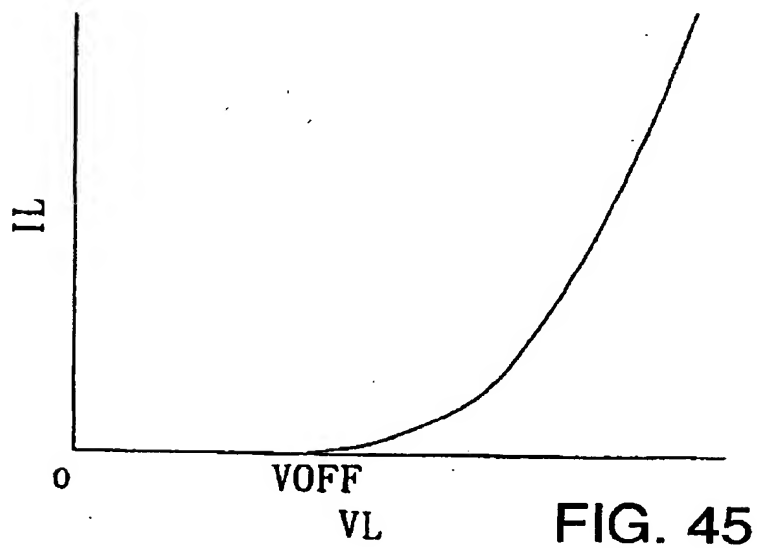
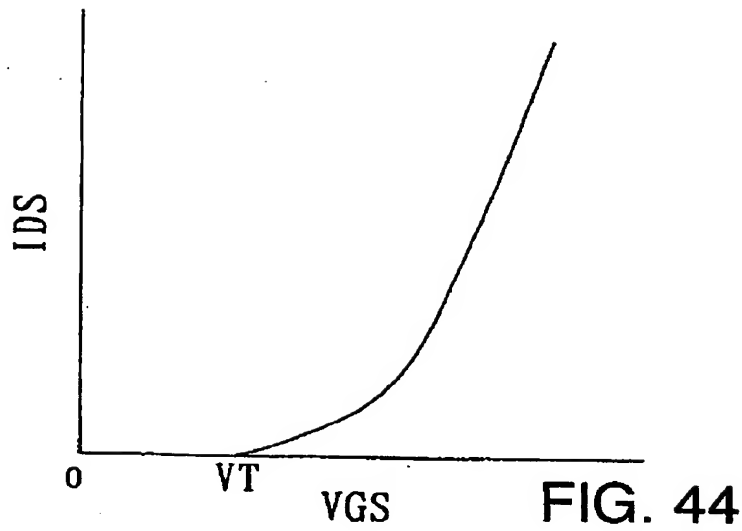


FIG. 43

10/506371





10/506371

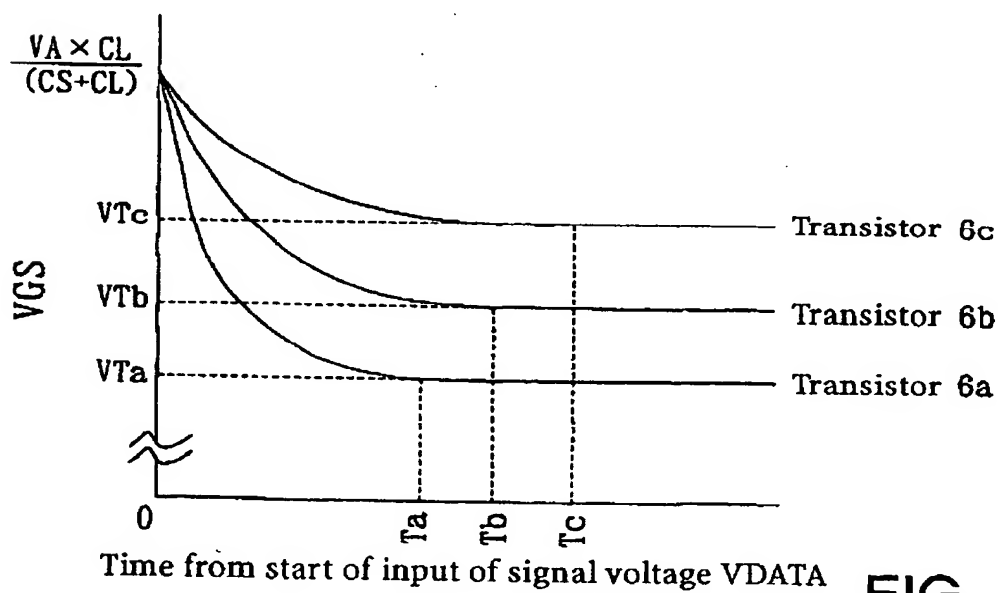
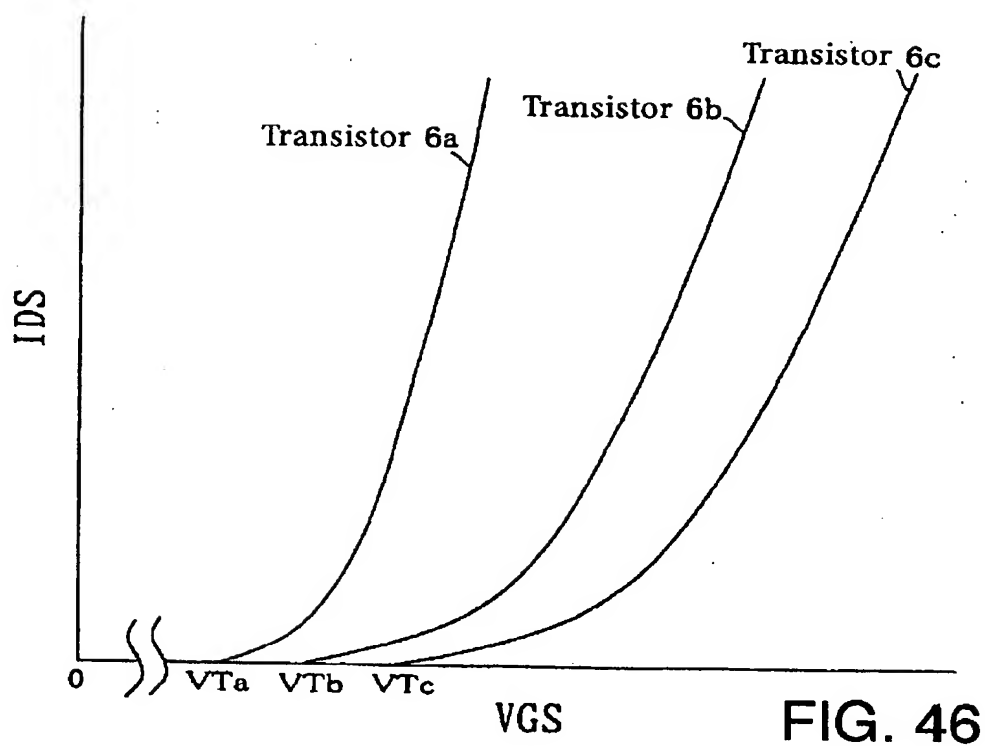


FIG. 47

10/506371

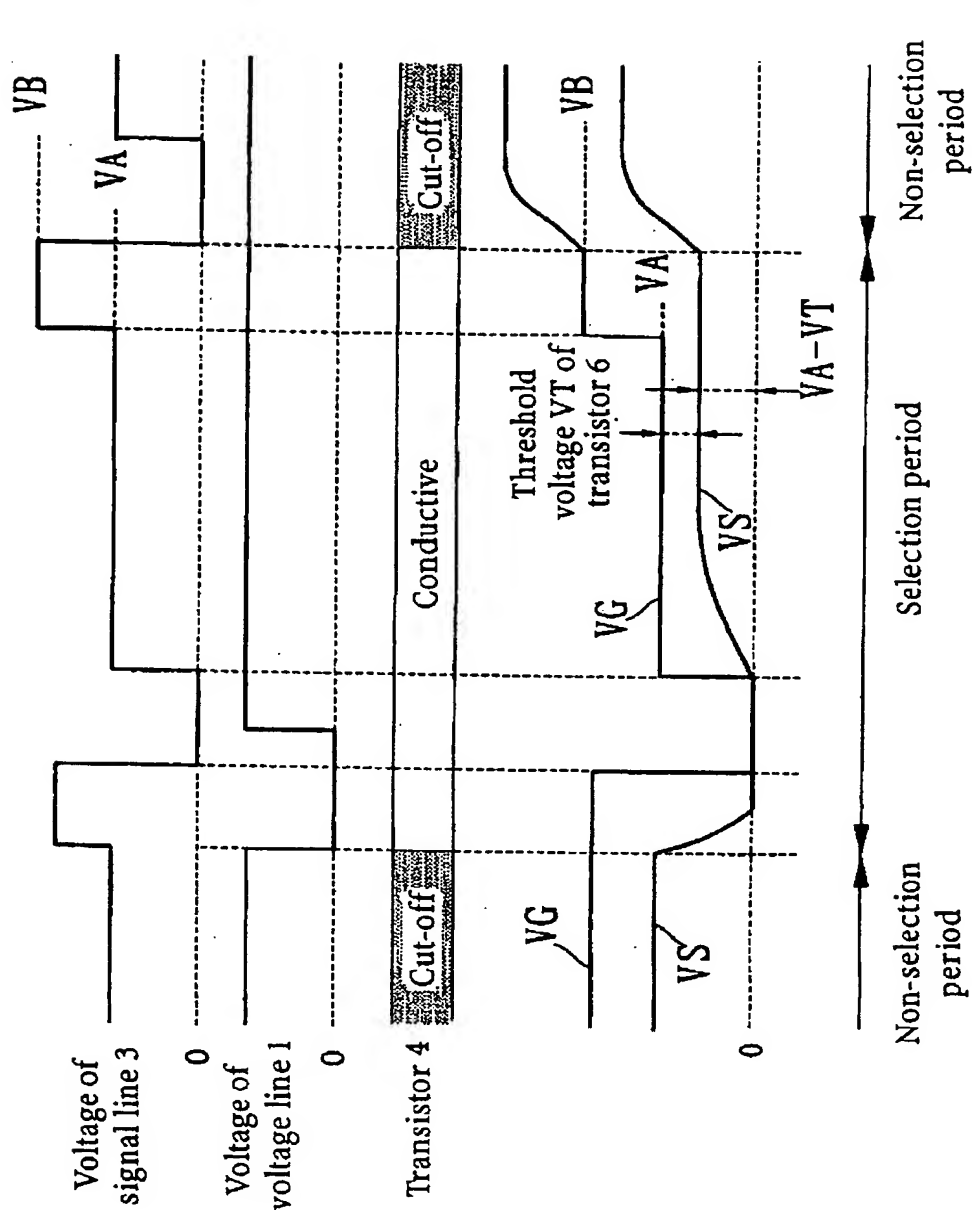


FIG. 48

10/506371

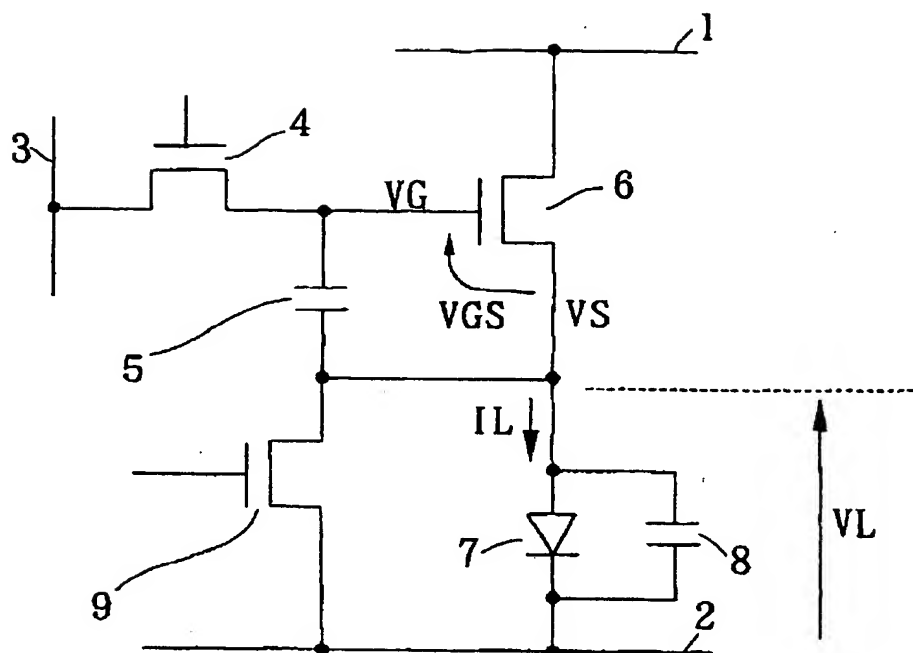


FIG. 49

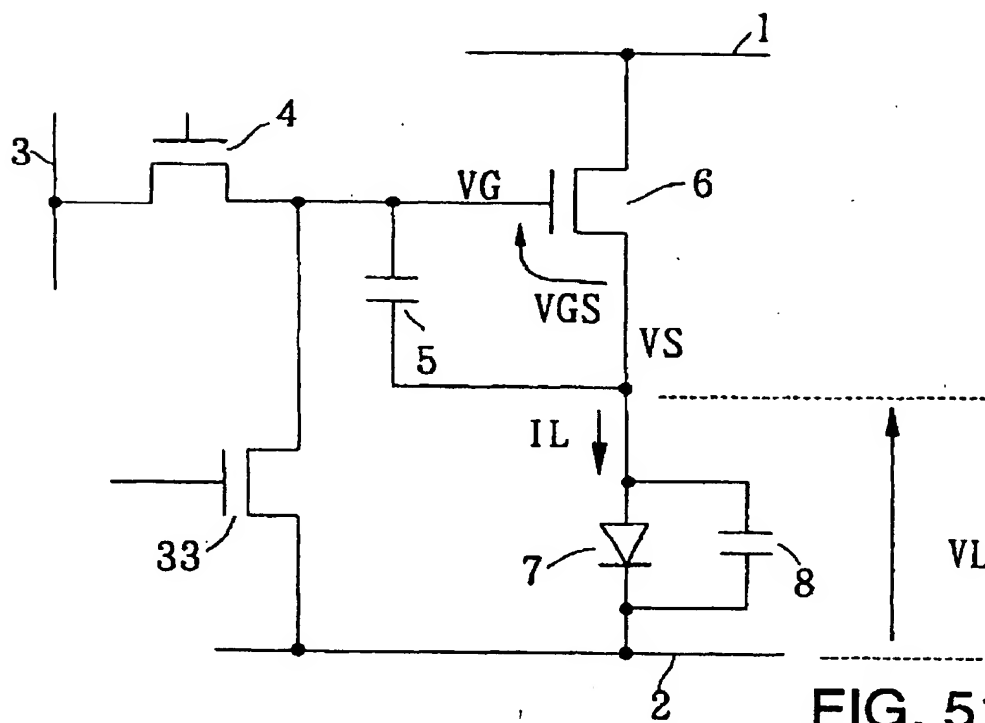


FIG. 51

10/506371

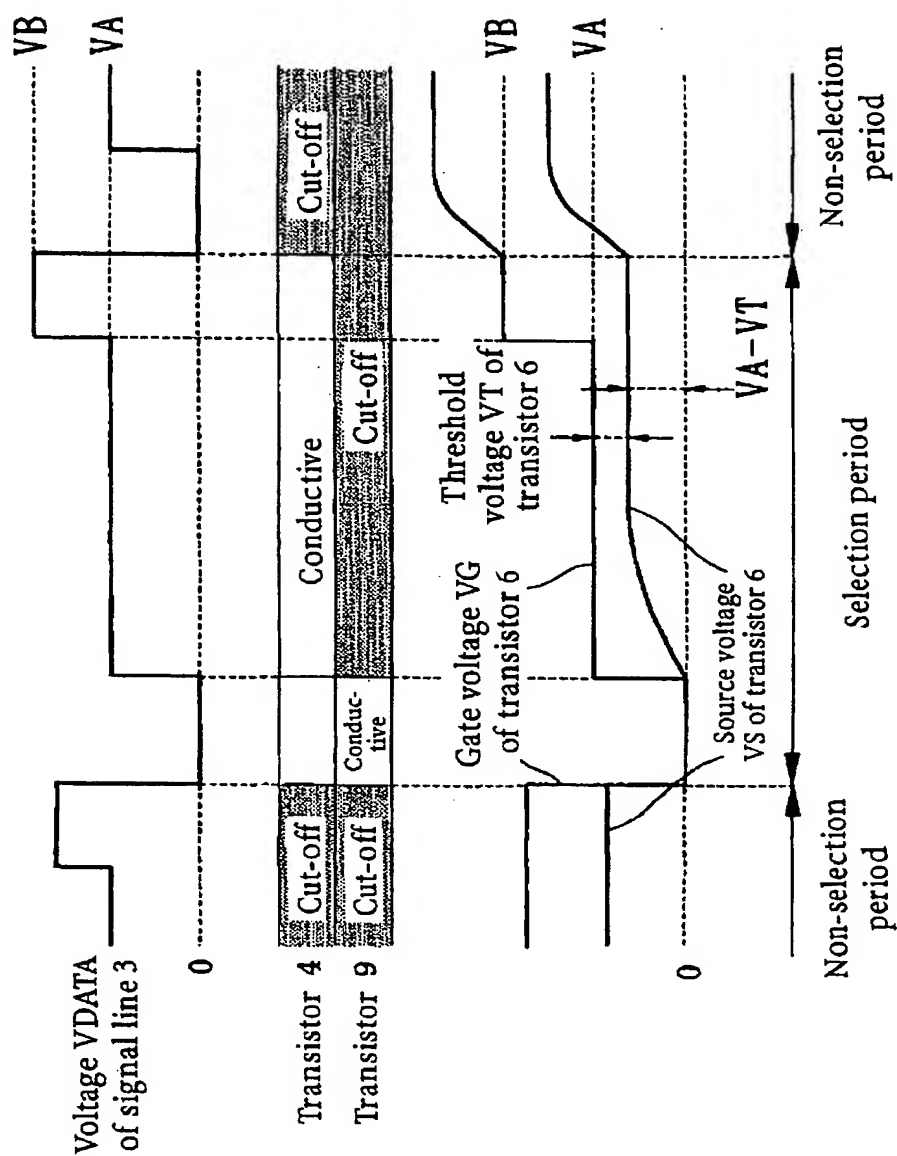


FIG. 50

10/506371

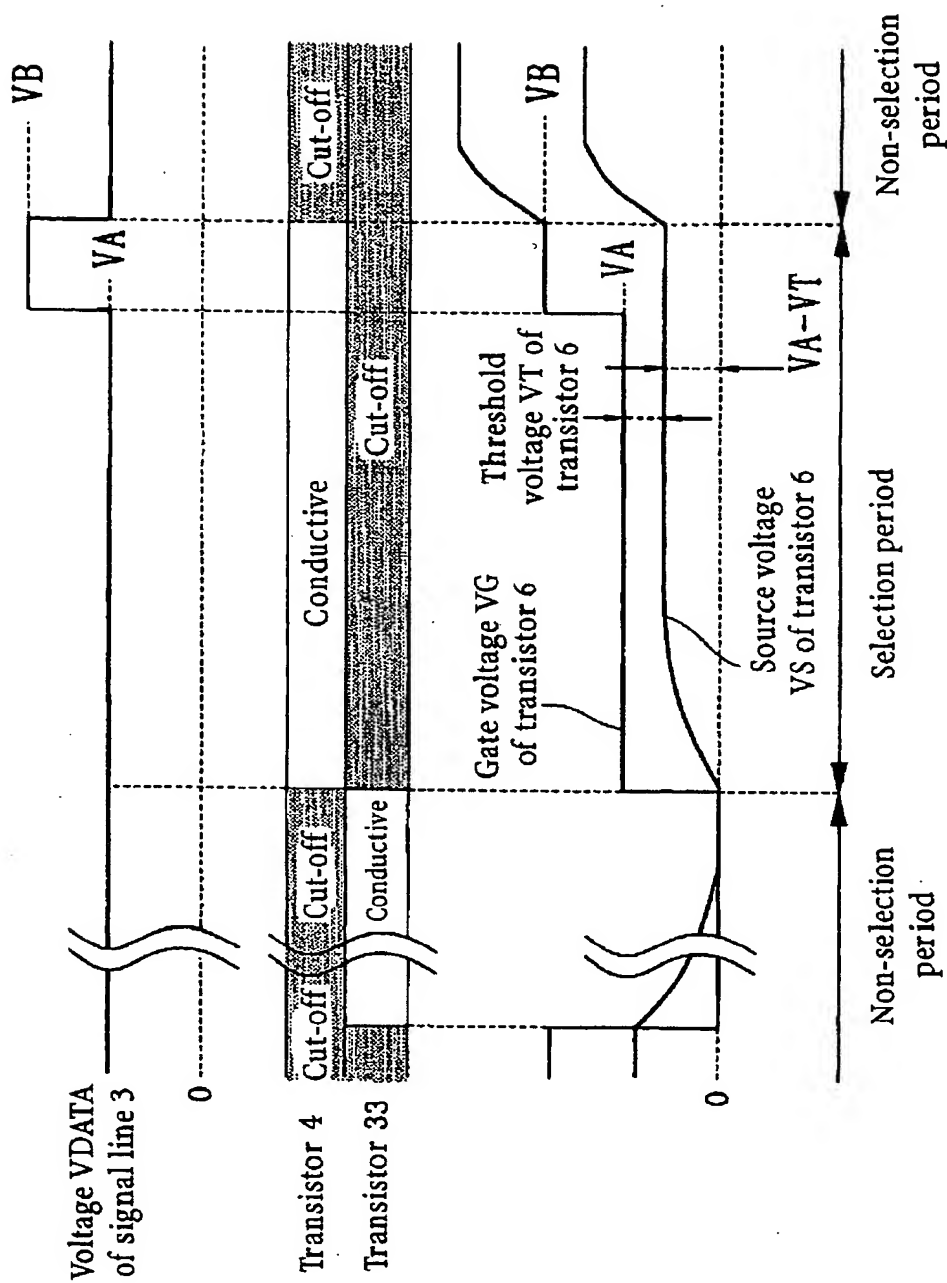


FIG. 52

10/506371

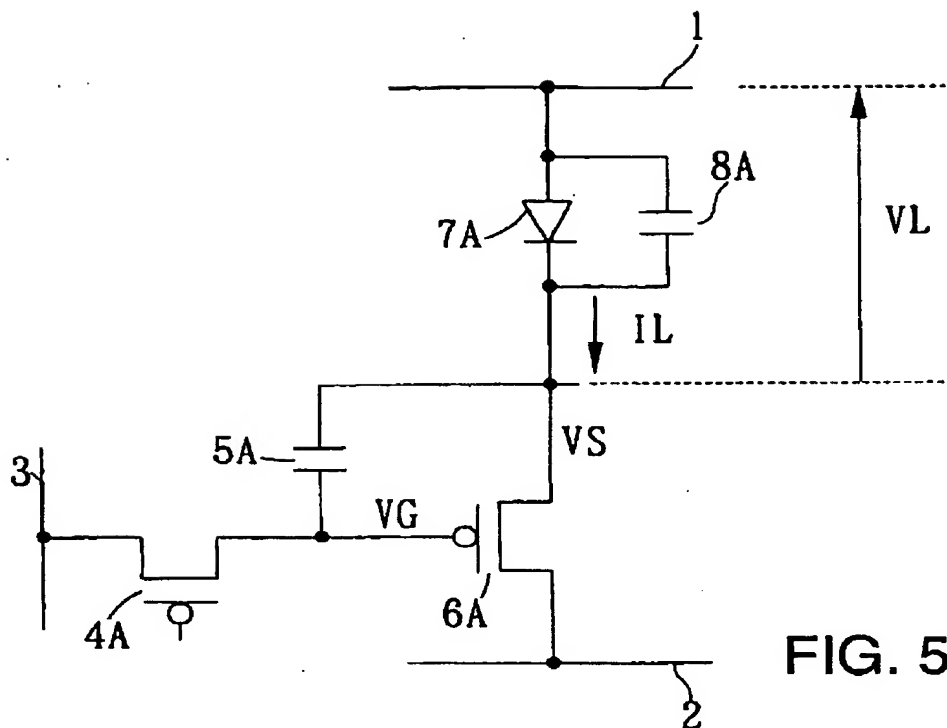


FIG. 53

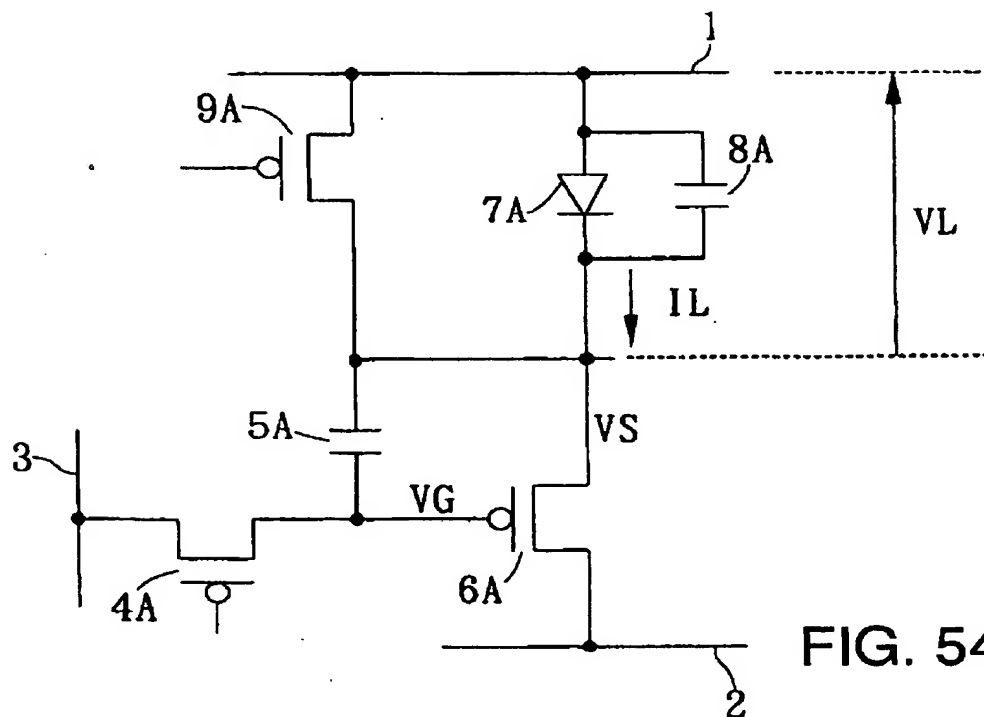


FIG. 54

10/506371

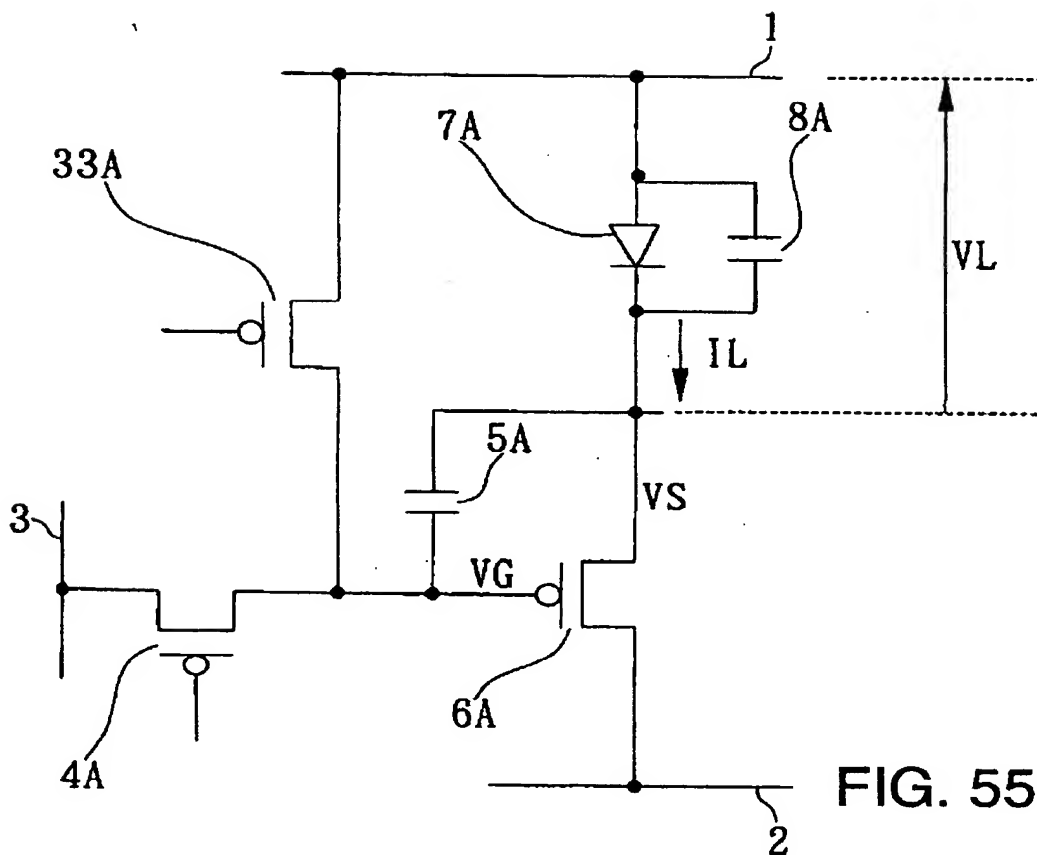


FIG. 55

**This Page is Inserted by IFW Indexing and Scanning  
Operations and is not part of the Official Record**

**BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- ☐ **BLACK BORDERS**
- ☐ **IMAGE CUT OFF AT TOP, BOTTOM OR SIDES**
- ☐ **FADED TEXT OR DRAWING**
- ☐ **BLURRED OR ILLEGIBLE TEXT OR DRAWING**
- ☐ **SKEWED/SLANTED IMAGES**
- ☐ **COLOR OR BLACK AND WHITE PHOTOGRAPHS**
- ☐ **GRAY SCALE DOCUMENTS**
- ☒ **LINES OR MARKS ON ORIGINAL DOCUMENT**
- ☐ **REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY**
- ☐ **OTHER:** \_\_\_\_\_

**IMAGES ARE BEST AVAILABLE COPY.**

**As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.**